



BARCO Projection Systems

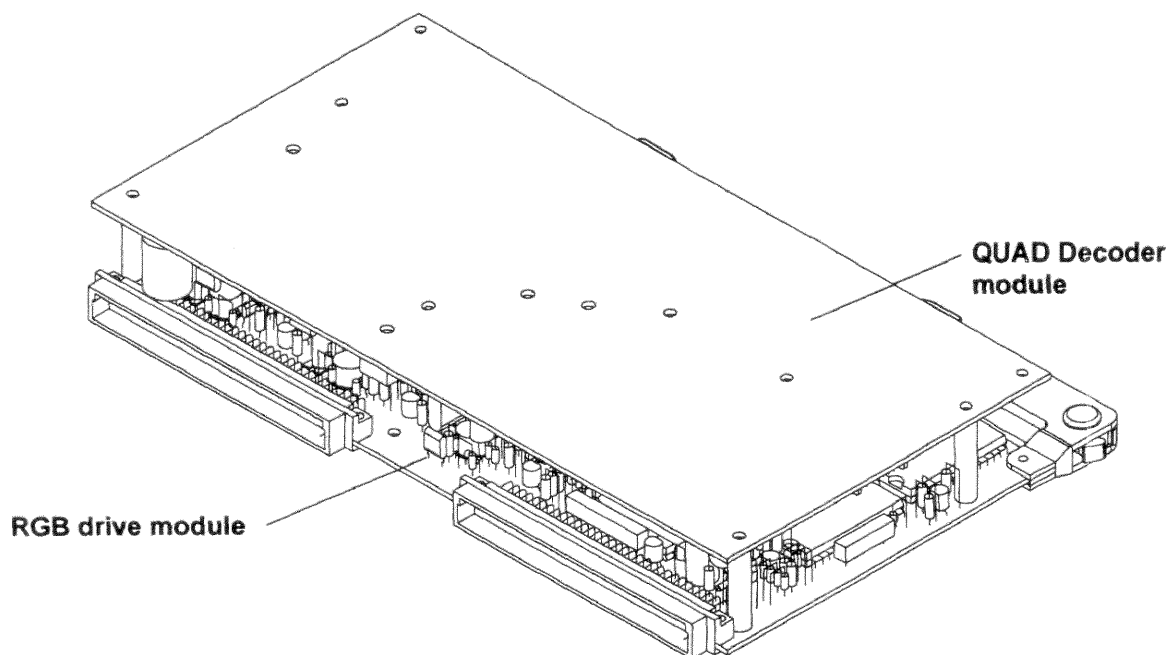
SECTION J

service sheet

RGB Driver+QUAD Decoder

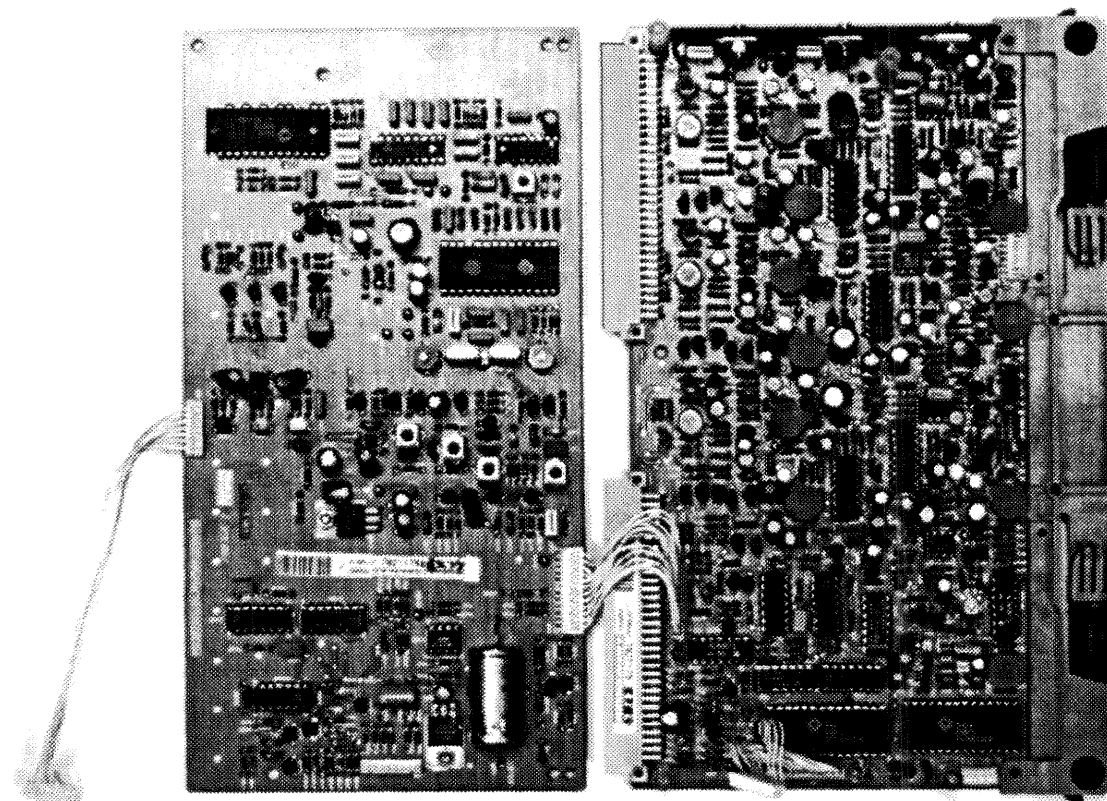
R7621175

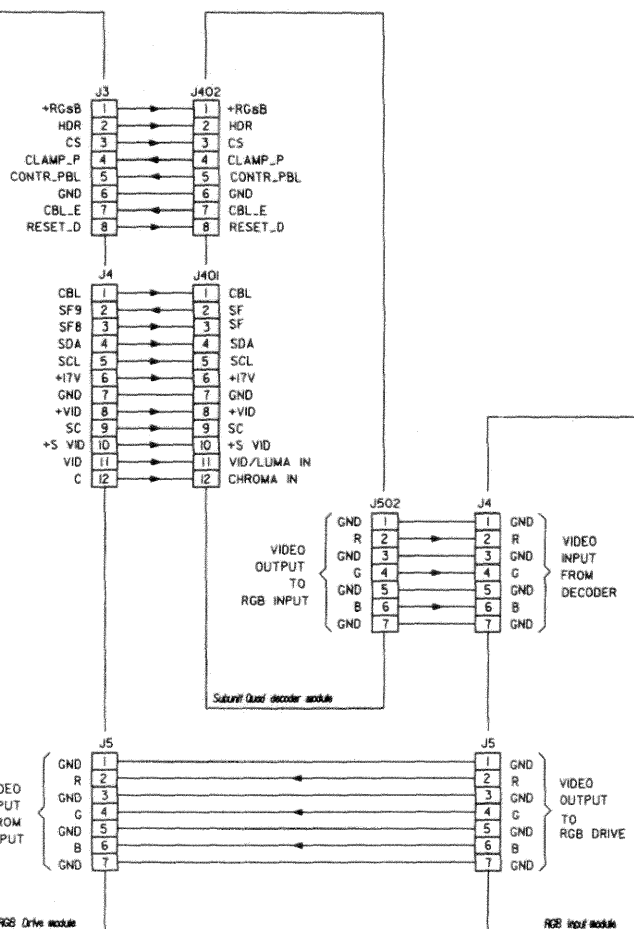
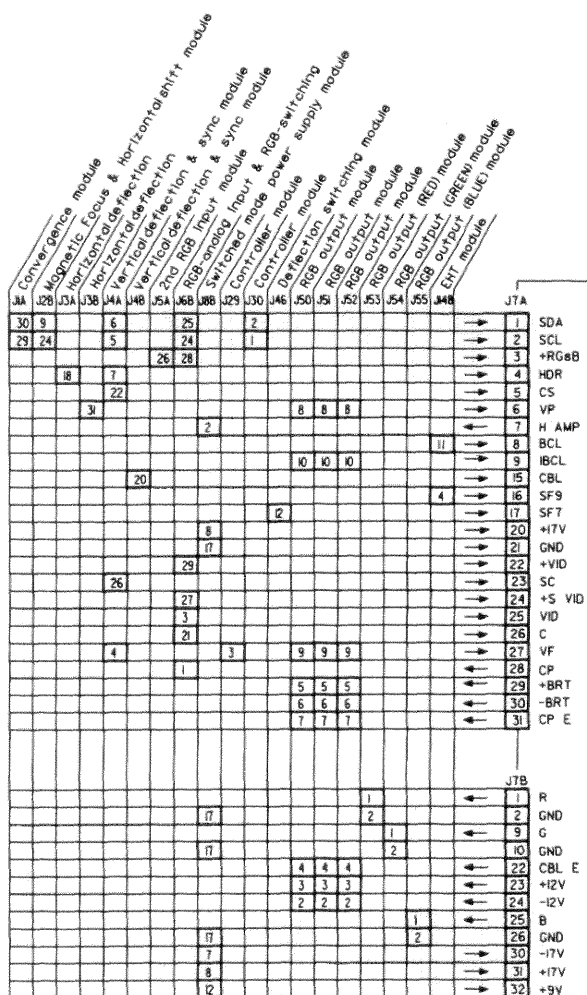
Sub module: Quad decoder R7621175S



QUAD Decoder module

RGB drive module





Name	Interconnection	Article nr.
RGB DRIVE & DECODER MODULE		76 21175-3
Date	Drawn	Checked
17-11-1993	JVDY	PDGY/JVST

BARCO PROJECTION SYSTEMS

C1	E 3	C414	C 4	D409	B 5	0206	F 4	R108	E 3	R323	E 6	R492	B 2
C2	E 2	C415	C 4	D410	B 5	0207	F 5	R109	E 3	R324	E 5	R493	C 2
C3	E 2	C416	C 4	D411	C 5	0208	F 4	R110	E 3	R325	E 5	R494	C 1
C4	E 2	C417	C 4	D412	D 3	0209	F 4	R111	E 3	R326	E 5	R495	C 1
C5	E 2	C418	C 4	D413	C 3	0210	F 4	R112	E 3	R327	E 5	R496	C 1
C6	E 2	C419	B 4	D414	C 3	0211	F 4	R113	E 3	R328	E 5	R497	B 1
C7	E 2	C420	B 4	D415	B 2	0212	F 4	R114	D 3	R329	E 5	R498	B 1
C8	E 2	C421	C 4			0213	G 4	R115	E 3	R330	F 5	R500	D 5
C9	E 2	C422	C 4	IC1	E 1	0214	F 4	R116	E 3	R331	F 5	R501	D 5
C10	F 2	C423	D 4	IC2	F 1	0215	E 4	R117	E 3	R332	F 5	R502	D 5
C11	F 3	C424	C 4	IC3	F 2	0216	F 4	R118	E 4	R333	F 5	R503	D 5
C12	F 6	C425	C 4	IC4	F 2	0217	G 2	R119	E 3	R334	F 5		
C13	F 6	C426	C 4	IC5	E 2	0218	F 4	R120	E 3	R335	F 5	569	E 1
C14	E 6	C427	C 4	IC6	D 2	0219	E 4	R121	E 3	R336	F 5		
C15	F 6	C428	D 4	IC7	F 2	0220	E 4	R122	E 4	R337	F 5		
C16	E 6	C429	B 4	IC8	E 2	0221	E 4	R123	E 4	R338	F 5	SR60	F 6
C17	E 6	C430	C 3	IC9	F 2	0222	E 4	R124	E 3	R339	F 5	SR61	F 6
C18	G 2	C431	B 4	IC10	E 2	0223	E 4	R125	E 3	R340	F 5	SR62	E 6
C19	F 3	C432	B 3	IC11	F 6	0224	E 4	R126	E 3	R341	F 5	SR400	C 5
C20	E 3	C433	B 3	IC12	F 6	0225	E 4	R127	E 3	R342	F 5	SR401	C 5
C21	F 2	C434	B 3	IC13	E 6	0226	E 4	R128	E 3	R343	F 5	SR402	B 3
C22	E 1	C435	C 3	IC14	E 3	0301	E 5	R129	F 3	R344	F 5	SR403	C 5
C23	F 2	C436	C 3	IC15	E 3	0302	E 5	R130	F 3	R345	F 5	SR404	B 2
C24	G 5	C437	C 3	IC16	F 3	0303	F 6	R131	F 3	R346	F 5	SR405	C 2
C25	E 2	C438	C 3	IC17	E 4	0304	F 6	R132	E 3	R347	F 5		
C26	E 2	C439	C 3	IC18	E 5	0305	F 6	R133	F 3	R348	F 5	XT400	C 3
C27	D 3	C440	C 3	IC19	E 5	0306	F 5	R134	F 3	R349	F 5	XT401	C 3
C28	D 3	C441	C 3	IC20	E 5	0307	F 5	R135	F 3	R350	F 5		
C29	E 3	C442	B 3	IC21	F 5	0308	F 5	R136	F 3	R351	F 5		
C30	E 3	C443	C 3	IC22	F 5	0309	F 5	R137	F 3	R352	F 5		
C31	D 3	C444	C 3	IC23	F 5	0310	F 5	R138	F 3	R353	F 5		
C32	E 4	C445	C 3	IC24	C 5	0311	F 5	R139	F 3	R354	F 5		
C33	E 4	C446	B 2	IC25	A 5	0312	F 5	R140	F 3	R355	F 5		
C34	E 4	C447	B 2	IC26	A 5	0313	F 5	R141	F 3	R356	F 5		
C35	E 4	C448	A 2	IC27	B 4	0314	F 5	R142	F 3	R357	F 5		
C36	E 4	C449	B 2	IC28	B 4	0315	F 5	R143	F 3	R358	F 5		
C37	E 4	C450	C 3	IC29	B 2	0316	F 5	R144	F 3	R359	F 5		
C38	E 4	C451	B 2	IC30	C 2	0317	F 5	R145	F 3	R360	F 5		
C39	E 4	C452	D 2	IC31	D 2	0318	F 5	R146	F 3	R361	F 5		
C40	E 4	C453	D 2	IC32	B 1	0319	F 5	R147	F 3	R362	F 5		
C41	E 4	C454	C 2	IC33	E 2	0320	F 5	R148	F 3	R363	F 5		
C42	E 4	C455	C 2	IC34	E 2	0321	F 5	R149	F 3	R364	F 5		
C43	E 4	C456	C 2	IC35	E 2	0322	F 5	R150	F 3	R365	F 5		
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C46	E 4	C459	C 2	IC38	E 2	0325	F 5	R153	F 3	R368	F 5		
C47	E 4	C460	C 2	IC39	E 2	0326	F 5	R154	F 3	R369	F 5		
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C62	E 4	C475	C 2	IC54	E 1	0341	F 5	R169	F 3	R384	F 5		
C63	E 4	C476	C 2	IC55	E 1	0342	F 5	R170	F 3	R385	F 5		
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C109	E 4	C522	C 2	IC101	E 1	0388	F 5	R216	F 3	R431	F 5		
C110	E 4	C523	C 2	IC102	E 1	0389	F 5	R217	F 3	R432	F 5		
C111	E 4	C524	C 2	IC103	E 1	0390	F 5	R218	F 3	R433	F 5		
C112	E 4	C525	C 2	IC104	E 1	0391	F 5	R219	F 3	R434	F 5		
C113	E 4	C526	C 2	IC105	E 1	0392	F 5	R220	F 3	R435	F 5		
C114	E 4	C527	C 2	IC106	E 1	0393	F 5	R221	F 3	R436	F 5		
C115	E 4	C528	C 2	IC107	E 1	0394	F 5	R222	F 3	R437	F 5		
C116	E 4	C529	C 2	IC108	E 1	0395	F 5	R223	F 3	R438	F 5		
C117	E 4	C530	C 2	IC109	E 1	0396	F 5	R224	F 3	R439	F 5		
C118	E 4	C531	C 2	IC110	E 1	0397	F 5	R225	F 3	R440	F 5		
C119	E 4	C532	C										

To RGB INPUT (J4)

SECAM
REFERENCE (R-Y)

SECAM
REFERENCE (B-Y)

REFERENCE
OSCILLATOR
NTSC 3.58

ADJUSTMENT VIDEO
SIGNAL RED 0.8Vpp

REFERENCE
OSCILLATOR
PAL

CHROMA REJECTOR
PAL/SECAM/N4

CHROMA REJECTOR
NTSC 3.58

CHROMA FILTER
PAL/N4

CHROMA FILTER
SECAM

ADJUSTMENT VIDEO
SIGNAL GREEN 0.8Vpp

ADJUSTMENT VIDEO
SIGNAL BLUE 0.8Vpp

FROM
RGB INPUT
(J5)

ADJUSTMENT BRIGHTNESS
PULSE 0.144Vpp (RED)

ADJUSTMENT VIDEO
SIGNAL RED 4Vpp

ADJUSTMENT BRIGHTNESS
PULSE 0.144Vpp (GREEN)

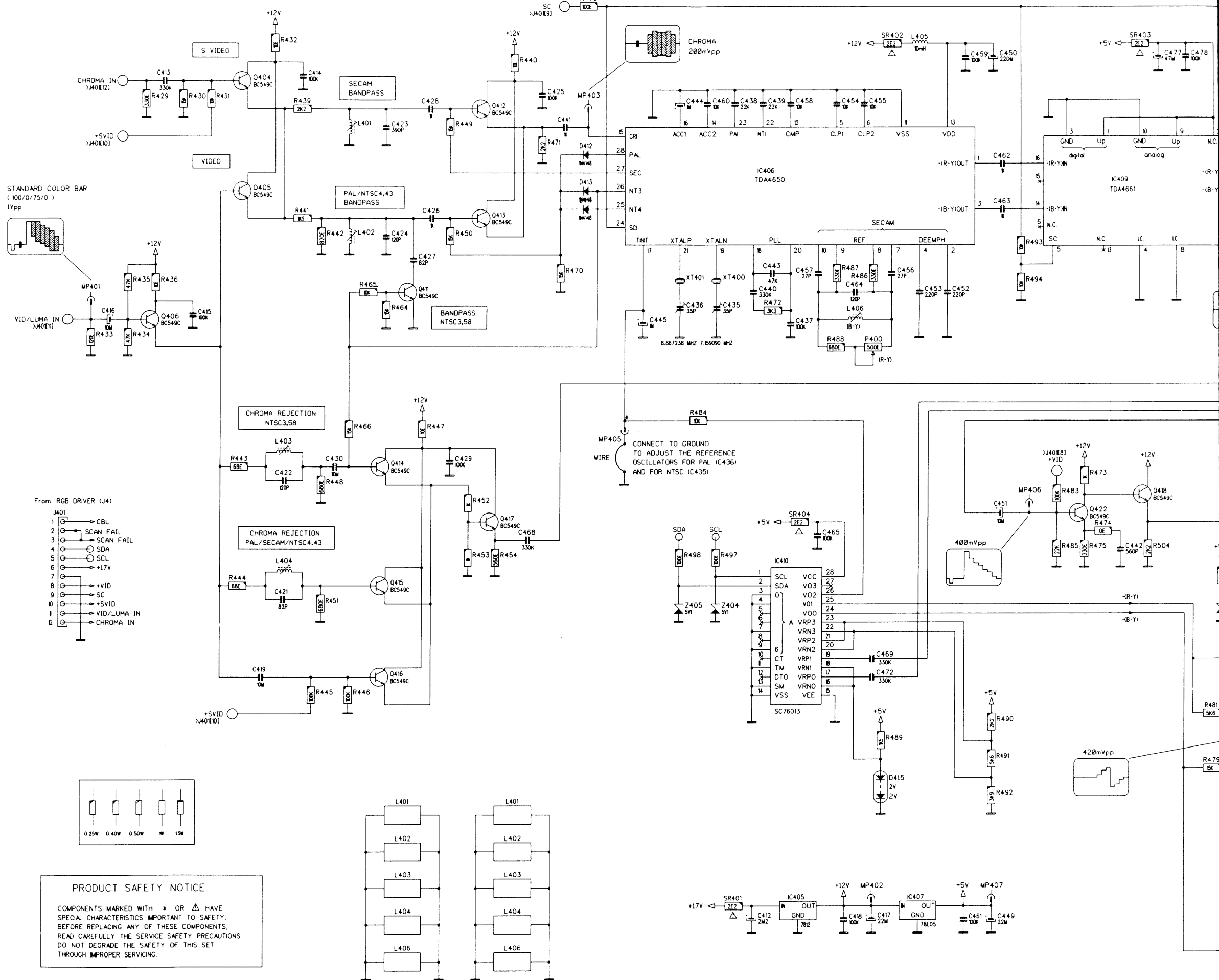
ADJUSTMENT VIDEO
SIGNAL GREEN 4Vpp

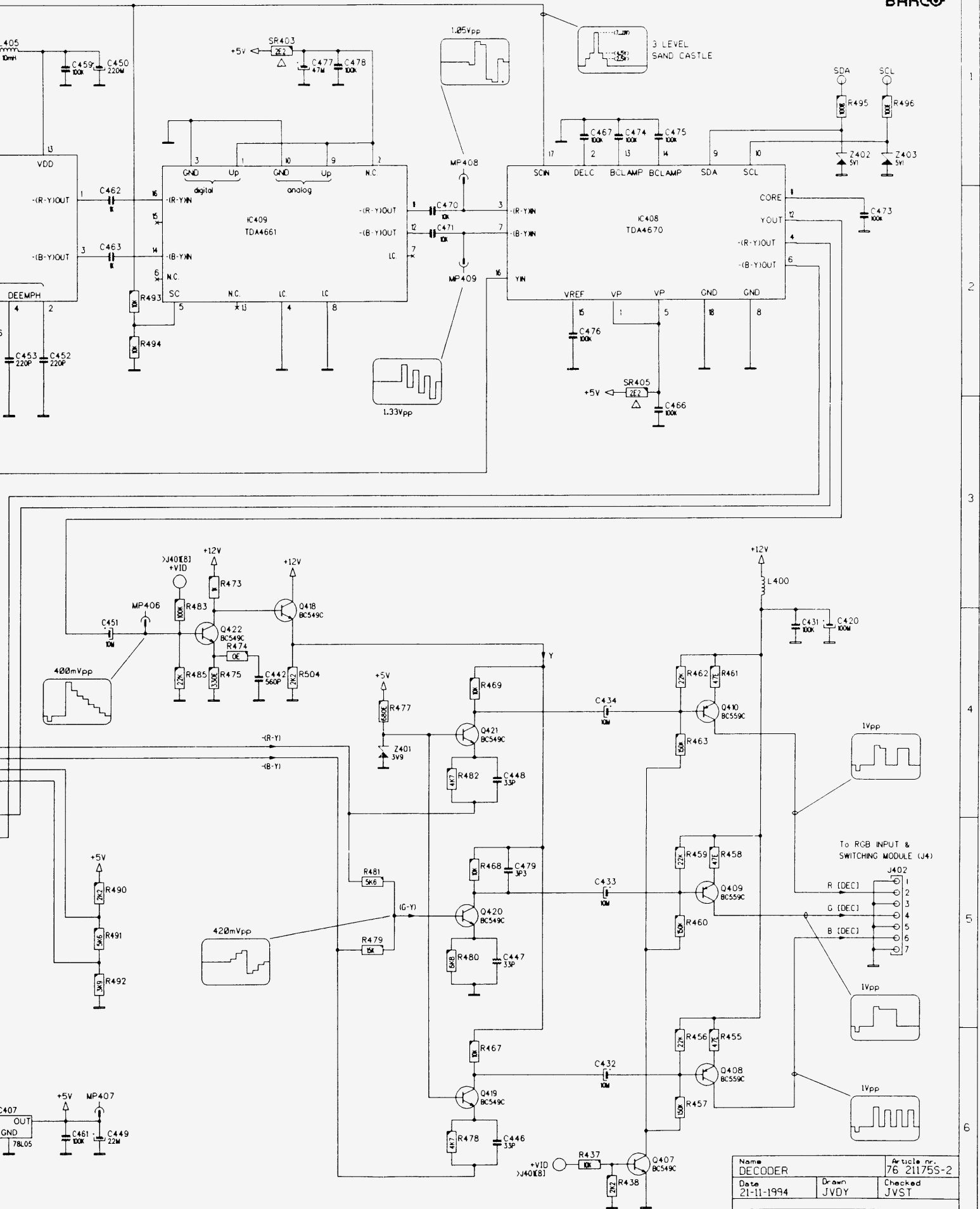
ADJUSTMENT BRIGHTNESS
PULSE 0.144Vpp (BLUE)

ADJUSTMENT VIDEO
SIGNAL BLUE 4Vpp

Name RGB DRIVER + DECODER		Article nr. 76 21175-3
Date 12-12-1994	Drawn JVDY	Checked PDGY
BARCO PROJECTION SYSTEMS		

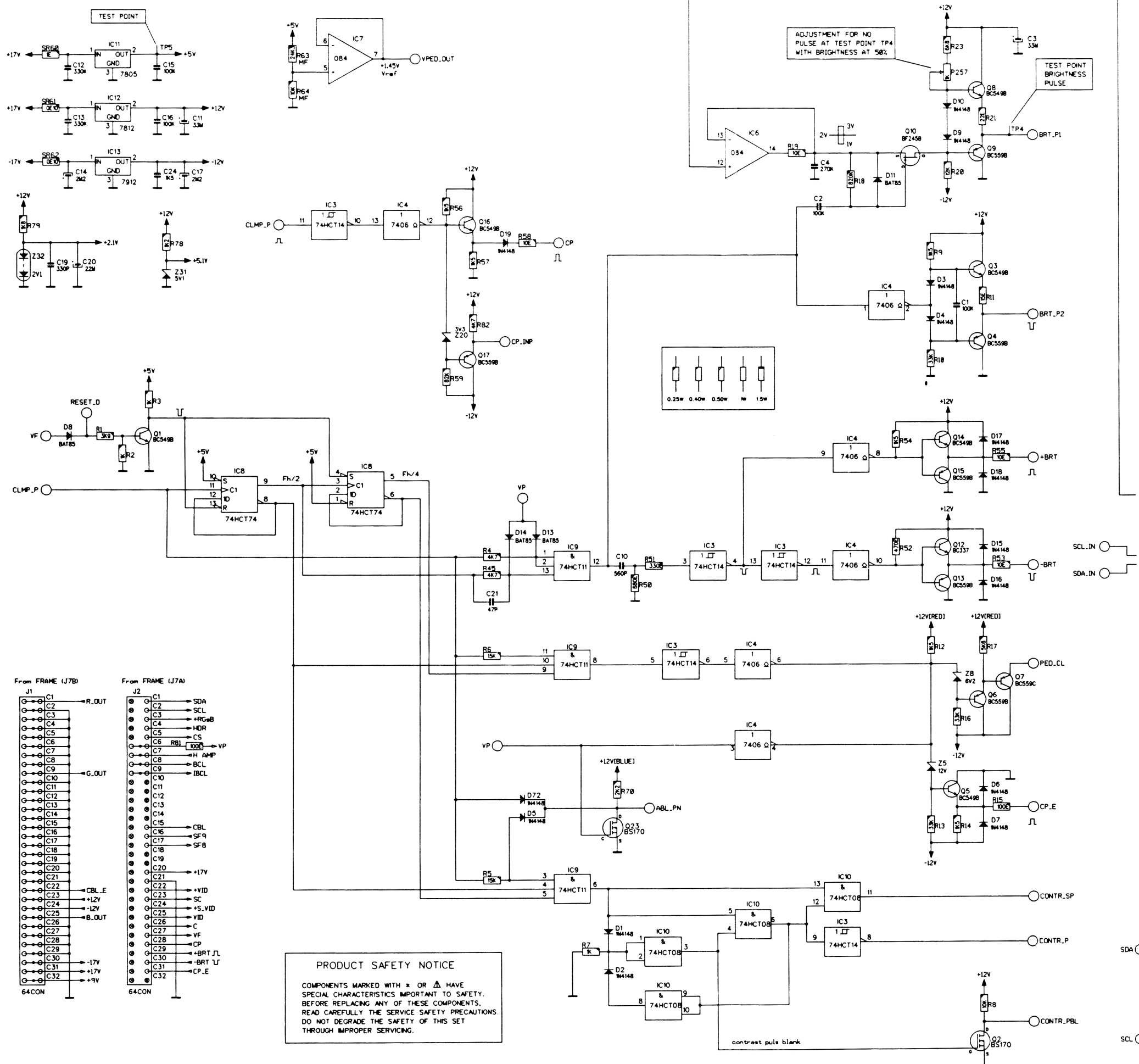
Modifications reserved

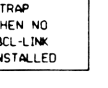
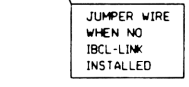
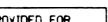




Name	DECODER	Article nr.	76 21175S-2
Date	21-11-1994	Drawn	JVDY
		Checked	JVST
BARCO PROJECTION SYSTEMS			

	COMP.	LOC.	SHEET	COMP.	LOC.	SHEET	COMP.	LOC.	SHEET
1	C400	F 1	sheet 2	IC407	F 6	sheet 1	R449	C 1	sheet 1
	C401	G 1	sheet 2	IC408	I 2	sheet 1	R450	C 2	sheet 1
	C402	G 3	sheet 2	IC409	G 2	sheet 1	R451	C 4	sheet 1
	C403	G 4	sheet 2	IC410	E 4	sheet 1	R452	C 4	sheet 1
	C404	B 3	sheet 2				R453	C 4	sheet 1
	C405	F 6	sheet 2	J400	D 1	sheet 2	R454	D 4	sheet 1
	C406	F 3	sheet 2	J401	A 4	sheet 1	R455	I 6	sheet 1
	C407	H 1	sheet 2	J402	J 5	sheet 1	R456	I 6	sheet 1
	C408	B 3	sheet 2				R457	I 6	sheet 1
	C409	D 2	sheet 2	L400	J 3	sheet 1	R458	I 5	sheet 1
	C410	B 4	sheet 2	L401	C 5	sheet 1	R459	I 5	sheet 1
	C411	A 2	sheet 2	L401	D 5	sheet 1	R460	I 5	sheet 1
	C412	E 6	sheet 1	L401	C 1	sheet 1	R461	I 4	sheet 1
	C413	B 1	sheet 1	L402	C 6	sheet 1	R462	I 4	sheet 1
	C414	B 1	sheet 1	L402	D 6	sheet 1	R463	I 4	sheet 1
	C415	B 2	sheet 1	L402	C 2	sheet 1	R464	C 2	sheet 1
	C416	A 2	sheet 1	L403	D 6	sheet 1	R465	C 2	sheet 1
	C417	F 6	sheet 1	L403	C 6	sheet 1	R466	C 3	sheet 1
	C418	F 6	sheet 1	L403	B 3	sheet 1	R467	H 6	sheet 1
	C419	B 5	sheet 1	L404	C 6	sheet 1	R468	H 5	sheet 1
	C420	J 4	sheet 1	L404	D 6	sheet 1	R469	H 4	sheet 1
2	C421	B 4	sheet 1	L404	B 4	sheet 1	R470	D 2	sheet 1
	C422	B 3	sheet 1	L405	F 1	sheet 1	R471	D 1	sheet 1
	C423	C 1	sheet 1	L406	D 6	sheet 1	R472	E 2	sheet 1
	C424	C 2	sheet 1	L406	C 6	sheet 1	R473	G 3	sheet 1
	C425	D 1	sheet 1	L406	F 2	sheet 1	R474	G 4	sheet 1
	C426	C 2	sheet 1				R475	G 4	sheet 1
	C427	C 2	sheet 1	MP400G	1	sheet 2	R476	D 1	sheet 1
	C428	C 1	sheet 1	MP401A	2	sheet 1	R477	H 4	sheet 1
	C429	C 3	sheet 1	MP402F	6	sheet 1	R478	H 6	sheet 1
	C430	C 3	sheet 1	MP403D	1	sheet 1	R479	H 5	sheet 1
	C431	J 4	sheet 1	MP404B	6	sheet 2	R480	H 5	sheet 1
	C432	I 6	sheet 1	MP405D	3	sheet 1	R481	H 5	sheet 1
	C433	I 5	sheet 1	MP406G	3	sheet 1	R482	H 4	sheet 1
	C434	I 4	sheet 1	MP407F	6	sheet 1	R483	G 3	sheet 1
	C435	E 2	sheet 1	MP408H	1	sheet 1	R484	E 3	sheet 1
	C436	E 2	sheet 1	MP409H	2	sheet 1	R485	G 4	sheet 1
	C437	E 2	sheet 1				R486	F 2	sheet 1
	C438	E 1	sheet 1	P400	F 3	sheet 1	R487	F 2	sheet 1
	C439	E 1	sheet 1				R488	E 3	sheet 1
	C440	E 2	sheet 1	Q400	G 4	sheet 2	R489	F 5	sheet 1
	C441	D 1	sheet 1	Q401	G 4	sheet 2	R490	F 5	sheet 1
3	C442	G 4	sheet 1	Q402	H 5	sheet 2	R491	F 5	sheet 1
	C443	E 2	sheet 1	Q403	B 3	sheet 2	R492	F 5	sheet 1
	C444	E 1	sheet 1	Q404	B 1	sheet 1	R493	G 2	sheet 1
	C445	D 2	sheet 1	Q405	B 2	sheet 1	R494	G 2	sheet 1
	C446	H 6	sheet 1	Q406	B 2	sheet 1	R495	J 1	sheet 1
	C447	H 5	sheet 1	Q407	I 6	sheet 1	R496	J 1	sheet 1
	C448	H 4	sheet 1	Q408	I 6	sheet 1	R497	E 4	sheet 1
	C449	F 6	sheet 1	Q409	I 5	sheet 1	R498	E 4	sheet 1
	C450	F 1	sheet 1	Q410	I 4	sheet 1	R500	A 5	sheet 2
	C451	F 4	sheet 1	Q411	C 2	sheet 1	R501	A 5	sheet 2
	C452	F 2	sheet 1	Q412	C 1	sheet 1	R502	B 5	sheet 2
	C453	F 2	sheet 1	Q413	C 2	sheet 1	R503	B 5	sheet 2
	C454	F 1	sheet 1	Q414	C 3	sheet 1	R504	G 4	sheet 1
	C455	F 1	sheet 1	Q415	C 4	sheet 1			
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	C457	E 2	sheet 1	Q417	D 4	sheet 1	SR401E	6	sheet 1
	C458	E 1	sheet 1	Q418	G 3	sheet 1	SR402F	1	sheet 1
	C459	F 1	sheet 1	Q419	H 6	sheet 1	SR403G	1	sheet 1
	C460	E 1	sheet 1	Q420	H 5	sheet 1	SR404E	4	sheet 1
4	C461	F 6	sheet 1	Q421	H 4	sheet 1	SR405I	2	sheet 1
	C462	F 1	sheet 1	Q422	G 4	sheet 1			
	C463	F 2	sheet 1	Q500	B 5	sheet 2	XT400E	2	sheet 1
	C464	F 2	sheet 1	Q501	B 5	sheet 2	XT401E	2	sheet 1
	C465	E 4	sheet 1						
	C466	I 3	sheet 1	R400	E 2	sheet 2	Z400	A 4	sheet 2
	C467	I 1	sheet 1	R401	F 3	sheet 2	Z401	H 4	sheet 1
	C468	D 4	sheet 1	R402	H 4	sheet 2	Z402	J 1	sheet 1
	C469	F 4	sheet 1	R403	G 4	sheet 2	Z403	J 1	sheet 1
	C470	H 2	sheet 1	R404	G 3	sheet 2	Z404	E 4	sheet 1
	C471	H 2	sheet 1	R405	H 5	sheet 2	Z405	E 4	sheet 1
	C472	F 5	sheet 1	R407	A 3	sheet 2			
	C473	J 2	sheet 1	R408	A 2	sheet 2			
	C474	I 1	sheet 1	R409	A 1	sheet 2			
	C475	I 1	sheet 1	R410	A 1	sheet 2			
	C476	I 2	sheet 1	R411	F 6	sheet 2			
	C477	G 1	sheet 1	R412	G 4	sheet 2			
	C478	H 1	sheet 1	R413	B 3	sheet 2			
	C479	H 5	sheet 1	R414	A 3	sheet 2			
5				R415	A 2	sheet 2			
	D400	F 3	sheet 2	R416	D 5	sheet 2			
	D401	H 4	sheet 2	R417	E 4	sheet 2			
	D402	H 4	sheet 2	R418	B 4	sheet 2			
	D403	G 4	sheet 2	R419	B 4	sheet 2			
	D404	A 3	sheet 2	R420	D 2	sheet 2			
	D405	E 6	sheet 2	R421	G 6	sheet 2			
	D406	G 4	sheet 2	R422	A 1	sheet 2			
	D407	E 6	sheet 2	R423	A 1	sheet 2			
	D408	E 4	sheet 2	R424	B 5	sheet 2			
	D409	G 6	sheet 2	R425	D 5	sheet 2			
	D410	E 4	sheet 2	R426	C 5	sheet 2			
	D411	F 1	sheet 2	R427	C 5	sheet 2			
	D412	D 1	sheet 1	R428	A 2	sheet 2			
	D413	D 2	sheet 1	R429	A 1	sheet 1			
	D414	D 2	sheet 1	R430	B 1	sheet 1			
	D415	F 5	sheet 1	R431	B 1	sheet 1			
6				R432	B 1	sheet 1			
	IC400	F 4	sheet 2	R433	A 3	sheet 1			
	IC400	F 4	sheet 2	R434	A 3	sheet 1			
	IC400	D 5	sheet 2	R435	A 2	sheet 1			
	IC400	F 6	sheet 2	R436	B 2	sheet 1			
	IC400	G 2	sheet 2	R437	I 6	sheet 1			
	IC401	G 1	sheet 2	R438	I 6	sheet 1			
	IC402	C 5	sheet 2	R439	B 1	sheet 1			
	IC402	C 5	sheet 2	R440	D 1	sheet 1			
	IC402	F 1	sheet 2	R441	B 2	sheet 1			
	IC403	B 2	sheet 2	R442	C 2	sheet 1			
	IC403	E 2	sheet 2	R443	B 3	sheet 1			
	IC403	G 2	sheet 2	R444	B 4	sheet 1			
	IC404	C 2	sheet 2	R445	B 5	sheet 1			
	IC404	G 2	sheet 2	R446	C 5	sheet 1			
	IC405	E 6	sheet 1	R447	C 3	sheet 1			
	IC406	E 2	sheet 1	R448	C 3	sheet 1			

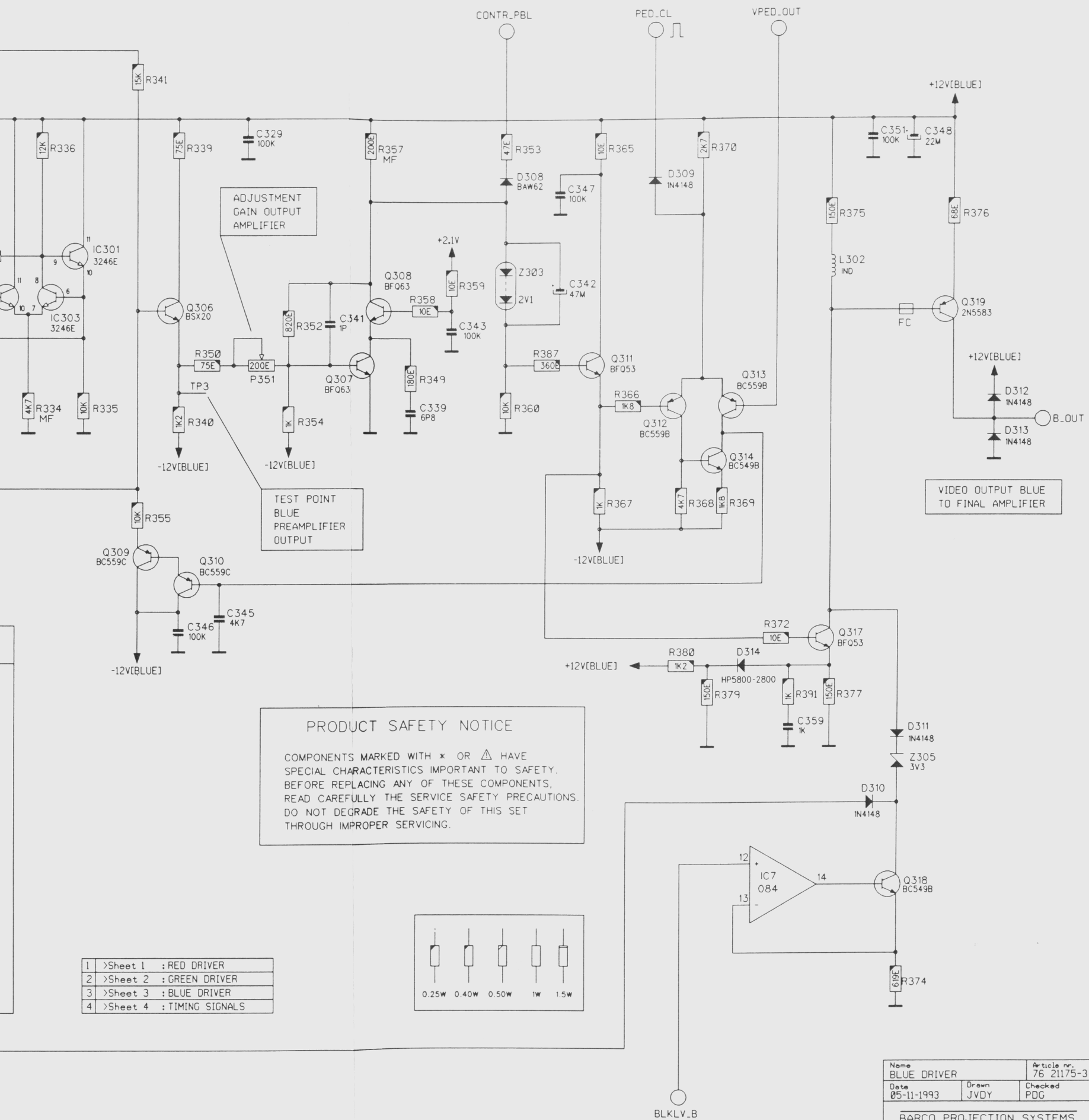




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Date 09-11-1993	Drawn JVDY	Checked PD
BARCO PROJECTION SYSTEMS		

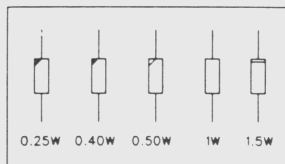
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2	>Sheet 2	: GREEN DRIVER
3	>Sheet 3	: BLUE DRIVER
4	>Sheet 4	: TIMING SIGNALS

SCHEMATIC NAME: 7621175.sch SHEET NUMBER: 4 DATE: Dec 21, 1994



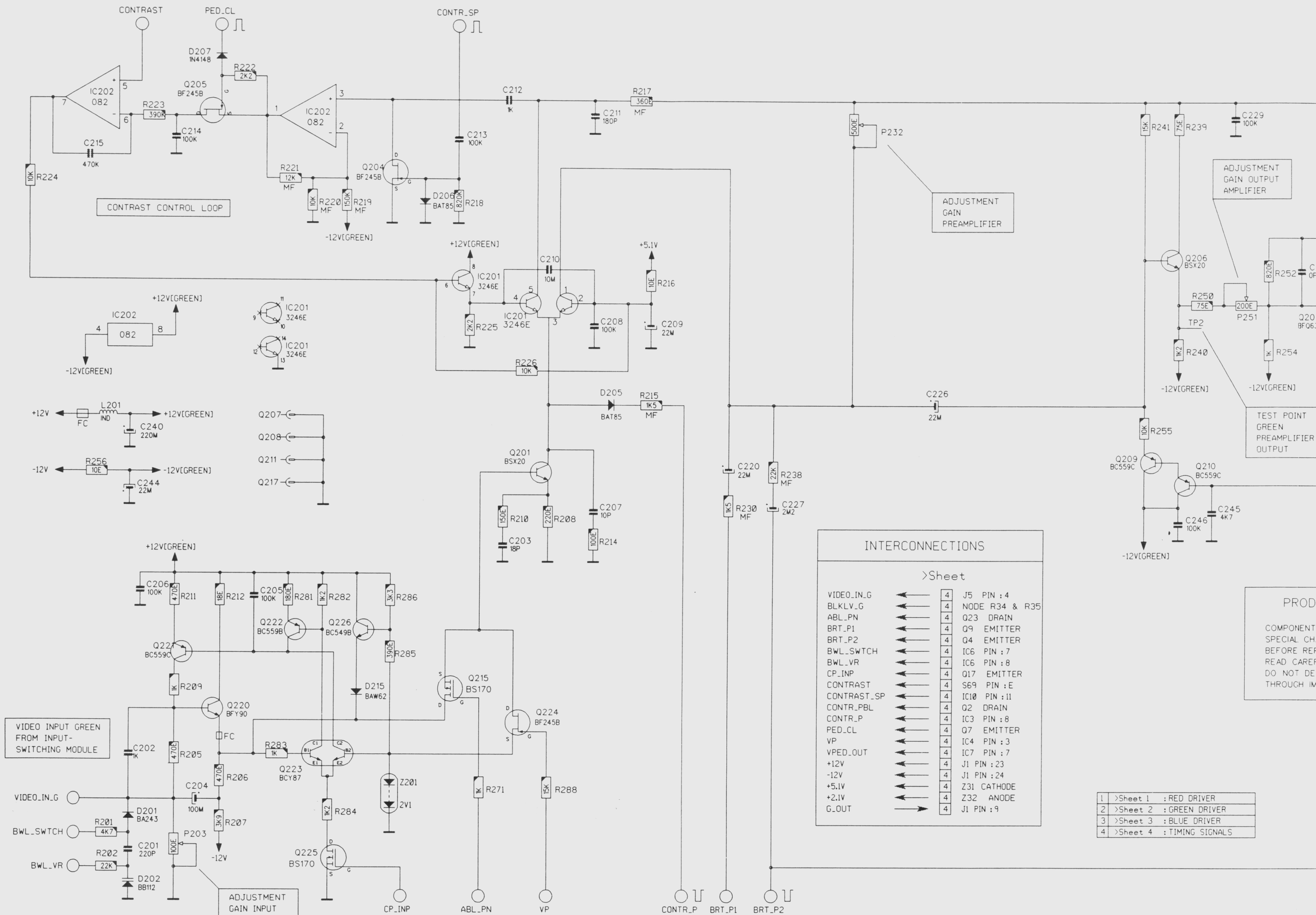
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1	>Sheet 1 : RED DRIVER
2	>Sheet 2 : GREEN DRIVER
3	>Sheet 3 : BLUE DRIVER
4	>Sheet 4 : TIMING SIGNALS

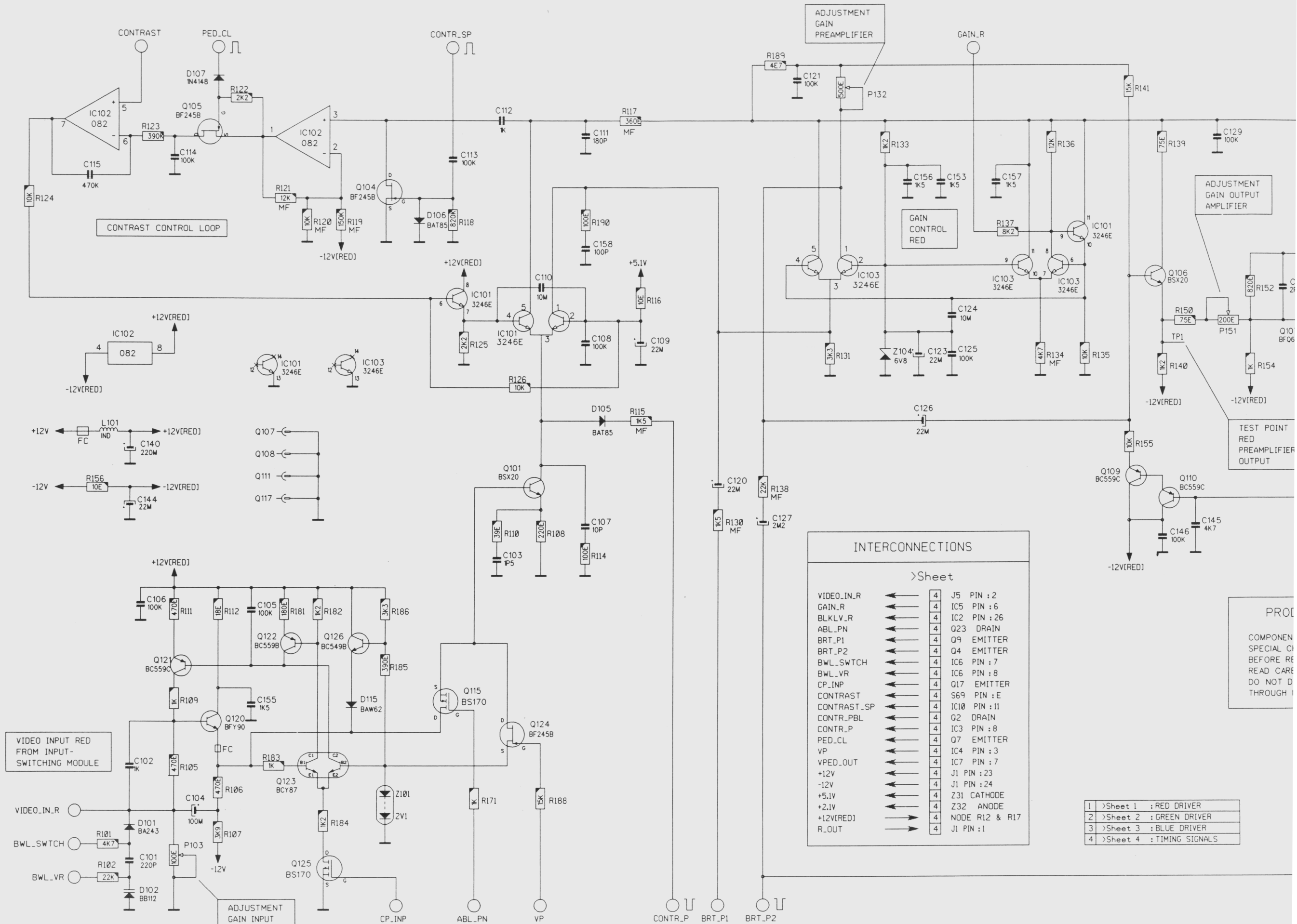
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Date	05-11-1993	Drawn	JVDY
		Checked	PDG
BARCO PROJECTION SYSTEMS			

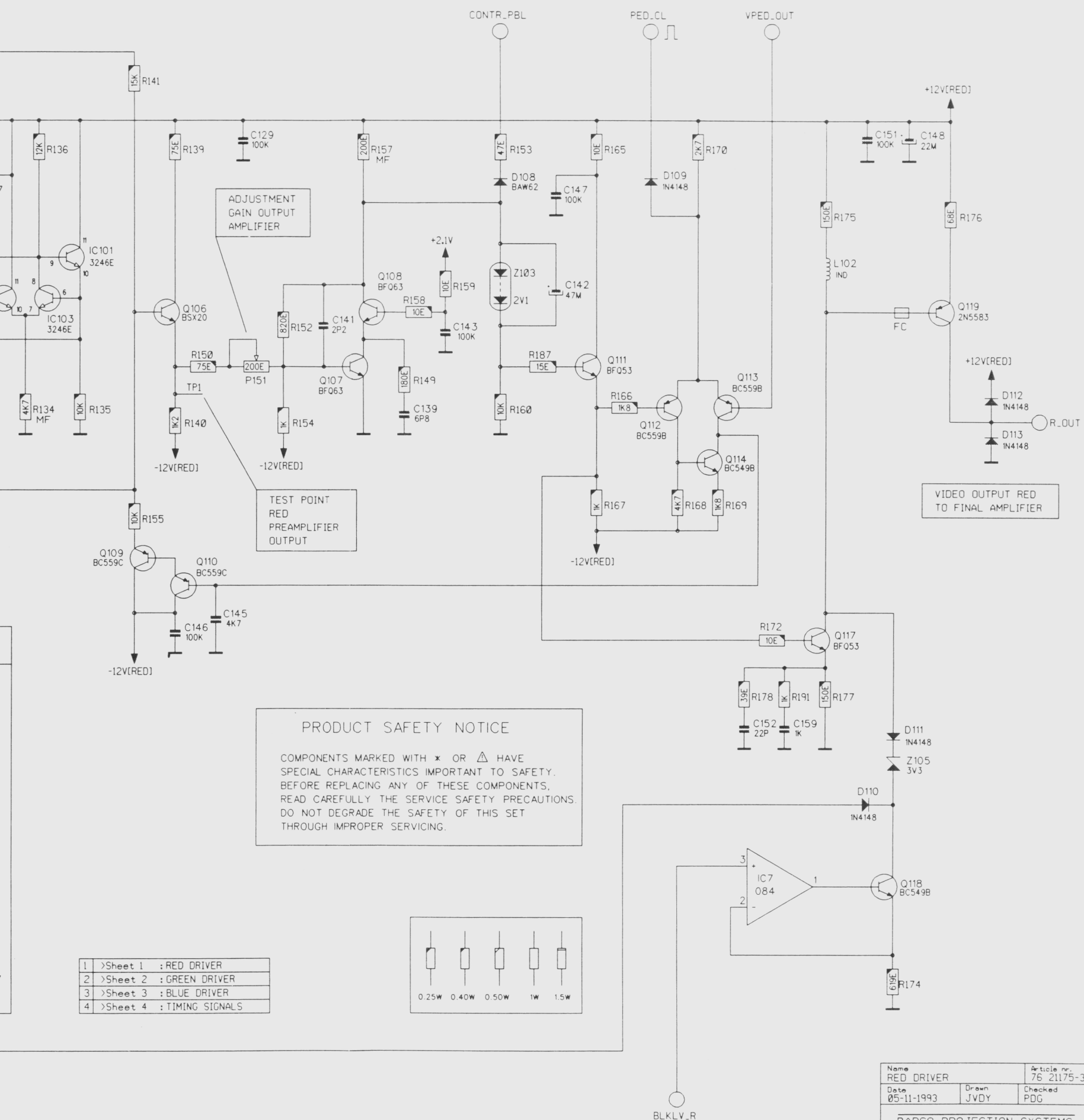


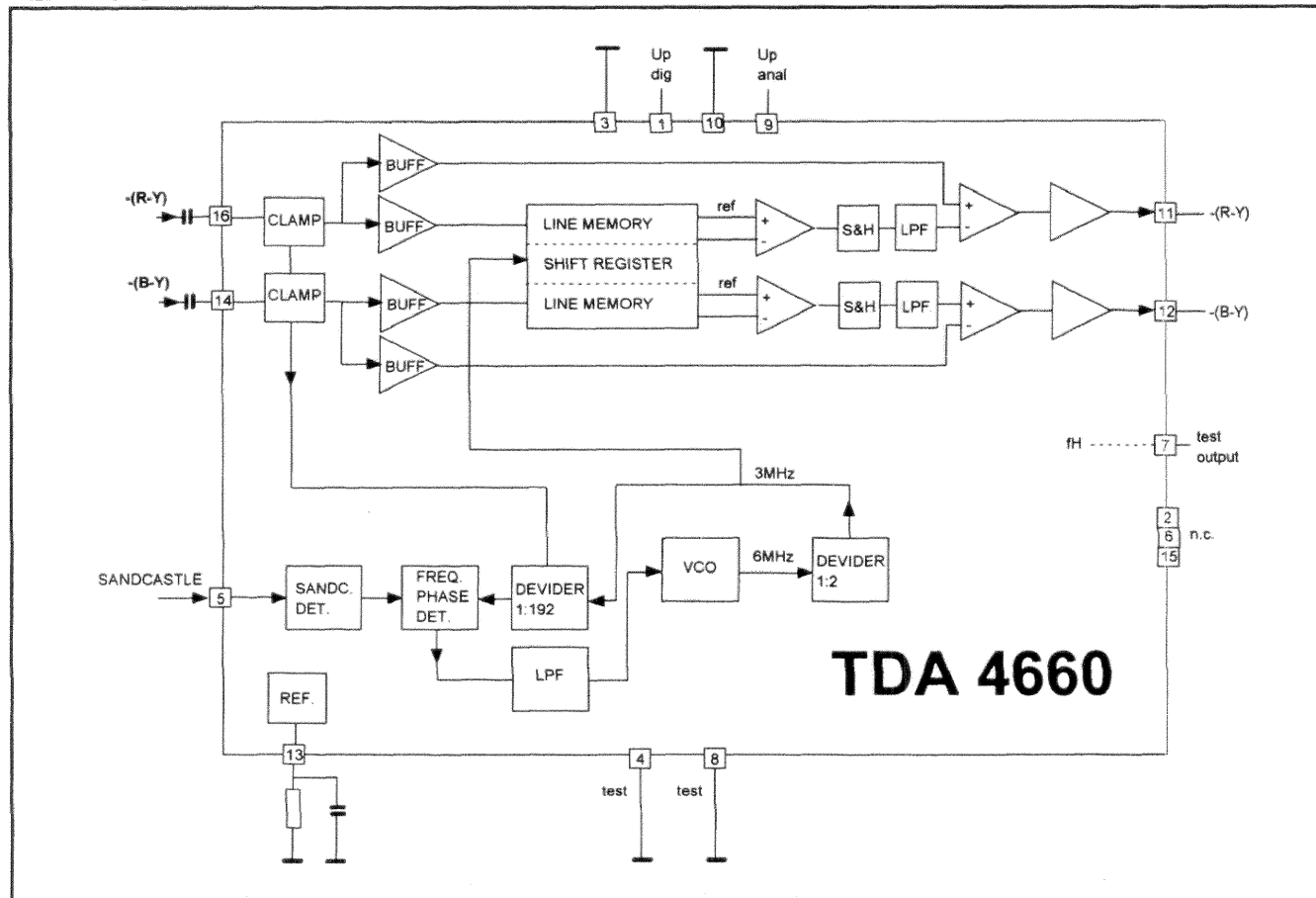
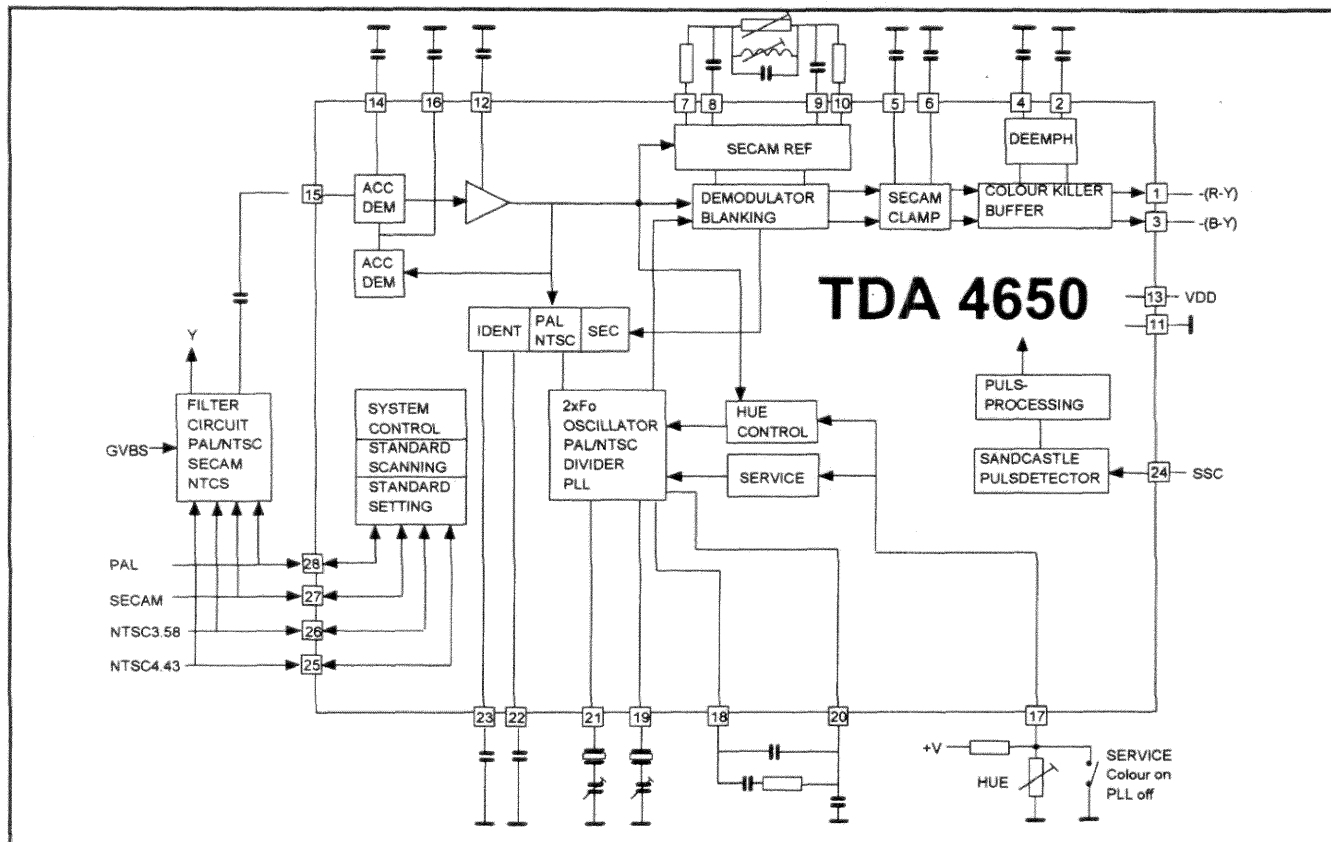


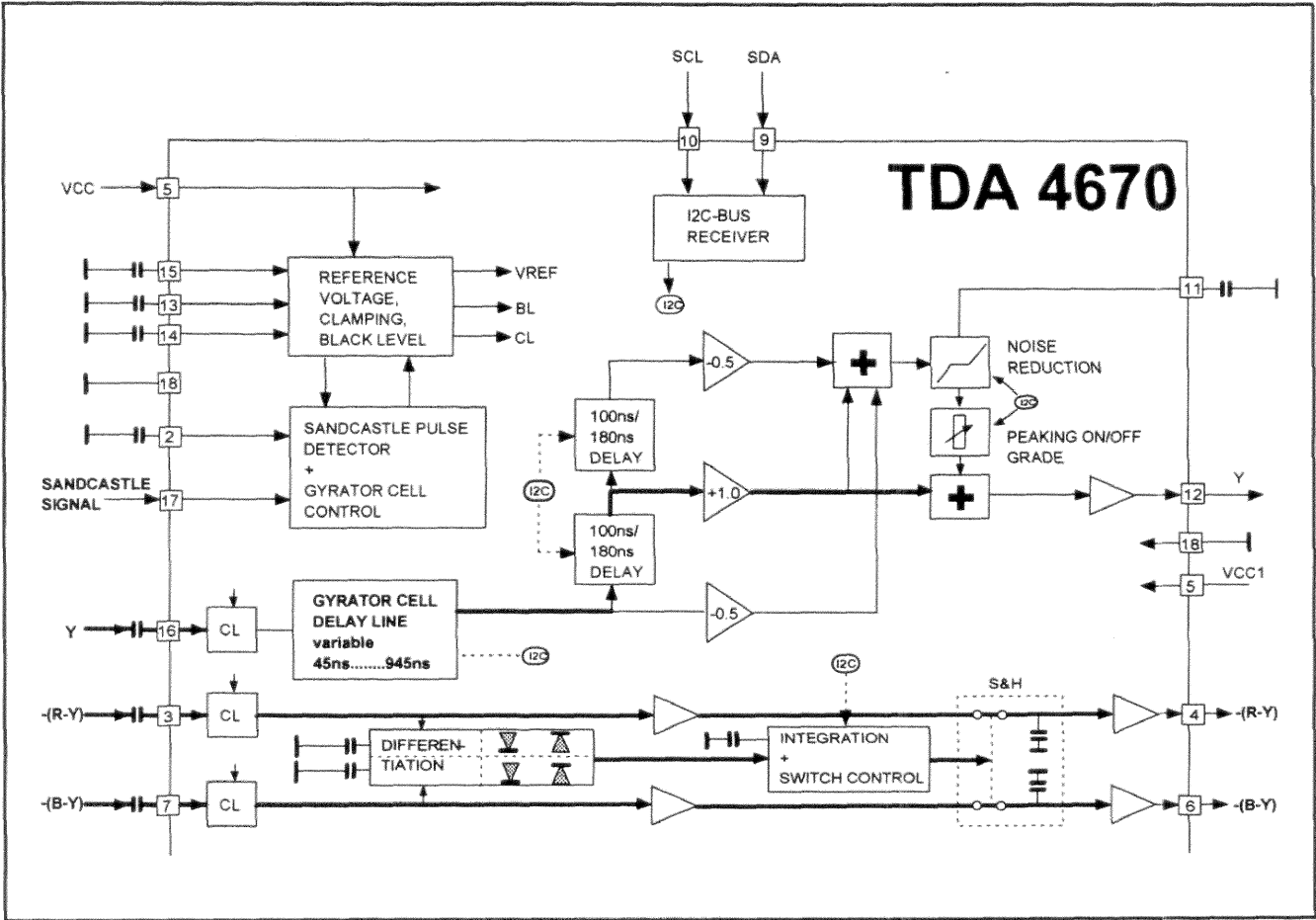
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G	H	I	J	K	L	Sheet 2
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Technical description "RGB drive +Quad Decoder"

General.

The red, green and blue signals, arriving from the decoder board, are here adjusted in amplitude (contrast adjustment). A brightness pulse with variable amplitude is inserted as preparation for the brightness control which is done in the power video amplifiers. Besides these "general" adjustments, an individual contrast and brightness is adjusted. These adjustments for red and green are referred to as GAIN_R, GAIN_B and BLKLV_R and BLKLV_B.

For the above contrast and brightness controls, various clamping pulses are generated as discussed hereafter.

A. Generation of clamping and blanking pulses CLMP_P and CBL_E.

a) CLMP_P pulse (sheet 2 of "Decoder + Timing Signals")

This clamping pulse is generated by a one-shot in IC403. The output pin 5 of IC403 "CLMP_P" is sent to the "RGB-driver" main board to generate other pulses (see below) and to pin 5 of the OR-gate in IC400 where it's combined with the CBL (Composite Blanking) coming from the "UN SYNC+VERT DEFL" board. After adding the CLMP_P, a push-pull stage improves the edges, and the total blanking is then referred to as CBL_E (_E = End stages) and applied to the video output power amplifiers.

The width of the CLMP_P is set by the time constant C409/R420, connected to pins 6 and 7 of IC403.

The leading edge or the start of this pulse is important, as it is used to trigger and generate other pulses. The start of this pulse is determined by the mode of the projector.

There are three possible modes : [Vid/SVideo] [RGBS] and [RGSB.]

The single one-shot is triggered on a positive transition (active high) of a pulse applied to pin 10. This trigger pulse is the output pin 7 of the MUX (Selector) IC404. The latter has two "selecting pins", 14 and 2 which are high or low. The combination of these levels allows the selection of one of the four input signals.

1. Video / SVideo :

In this case pin 14 of IC404 is set "high" with the +VID voltage and pin 2 is "low". The output pin 7 is now the input signal at "1" (pin 5) or the SC (Sandcastle). The zener diode Z400 of 5.6 volt 'cuts' the lower level of the SC so that only the top of the sandcastle passes the MUX and this top triggers the one-shot. The start of the CLMP_P pulse then coincides with the start of the top pulse (smallest part) of the sandcastle pulse. This is required for a video signal, as this clamping pulse must clamp the backporch of the sync pulse. When using the SC as a reference, clamping on the top of the sync is impossible.

2. RGsB (Sync on Green) :

The inputs on the selecting pins 14 and 2 of IC404 are now reversed, pin 2 is "high" and pin 14 is "low". The output is now the "2" input or the CS (Composite Sync) input.

As the CS pulses are negative, the one shot is triggered on the positive transition, this is the trailing edge of the sync pulse. This prohibits clamping on the top of the sync pulse and the clamping starts just at the end of the sync pulse (note that the sandcastle is .

Note : If the CS signal is absent for some time , i.e., during switching of sources or for any other reason, the ABL circuit can run into trouble since the clamping pulses would be discontinued. Therefore, a transistor Q530 pulls pin 2 of IC401 to ground level to "force" the HDR (see 3 below.) to trigger the one shot. As long as CS pulses are applied at its base, the transistor is blocked. When the signal is not present on the base of the transistor, it inhibits the +RGsB voltage

3. RGBS :

In this case, both selecting pins on IC401 are "low" and the "0" input (pin 6) is selected. This input is the output pin 4 of the other one-shot in IC400. The latter causes a delay of the HDR (Hor. Drive) pulse as a compensation for the switching delay of the MOSFETS in the deflection circuit.

The CLMP_P pulse therefore starts a little bit later than the drive pulse.

b) CBL_E (Composite Blanking for the End stages) pulse.

As explained before, the CLMP_P is now added to the CBL generated on the "UN SYNC+VERT DEFL" board. The CBL signal is limited to 5 volts with D408/410 and sent to pin 1 of an OR-gate in IC403. The signal can pass this gate on condition that the other input, pin 2, is "low". There are two conditions to be fulfilled for keeping this pin 2 "low" : no Scan Fail and +17 volts is not too low (lower than 14V or absent).

c) Blanking under certain conditions (SF and or +17V too low):

Both of the above conditions are permanently monitored with two level detectors of IC402 (LM393).

A portion of the +17 volts is applied on pin 2 and the Scan Fail ("high" when no Scan Fail) voltage to pin 6. Under normal conditions, both of the output pins, 1 and 7, of the level detectors are "low". The output from the OR-gate IC400, pin 11, is then also "low".

If either level detector output goes "high", meaning that there is scan fail or "a too low 17 volts", a permanent "high" would be placed on pin 6 of IC403, and thus a permanent blanking situation would occur. Transistor Q405 would be forward biased, due to the "high" on its base, and CBL_E would remain "high".

The "high" on pin 11 of IC403 is sent to Q408 and the "strobe" input pin 15 of the MUX (IC401). The "high" strobe input on IC401 places a "low" on pin 9 which would reset the one-shot in IC403.

The "high" at the gate of Q402 causes a low level at its drain to inhibit the CONTR_PBL pulse when there is a permanent blanking.

The same "high" also puts the RESET_D line "high" via D406 in order to reset the RS-Flipflop in IC8 on the main board (see below).

d) Mixing of SC and VF (Vertical Flyback) :

The decoder IC406 (TDA4650) on the decoder sub-unit needs both a SC (at line frequency) and a VF (vertical pulses) combined signal. On the main board of this unit, the VF pulses are applied to the base of Q1. These VF pulses are thus also available at the RESET_D line.

They pass an OR-gate in IC400 (pin 10) and are added to the SC pulses via a diode D409.

B. Generation of pulses for brightness control : CP, CP_INP, -BRT, +BRT, BRT_P1, BRT_P2.

Principle: A brightness pulse with variable amplitude is inserted every two lines so as to control the DC level of the video signal at the cathodes in the video end stages. Then, either, a contrast pulse is inserted in between the two brightness pulses or no pulse is inserted in order to allow the measurement of the black level for the contrast control loop, with the PED_CL pulse (PEDestal CLamping). The sequence of these pulses is obtained with(line) dividers by two (2). These dividers are RS-Flipflops with a feedback from the inverted output to the data input.

The CLMP_P pulse, generated on the subunit as discussed previously, is sent to a Schmitt trigger in IC3 (pin 11) for shaping and inversion. An inverter of IC4 pins 13 and 12, introduces a delay and the CP pulses are made available via a buffer Q16. For clamping purposes of the video inputs a CP_INP pulse with another DC level is obtained with Z10 / Q17 / -12volts.

The output of the RS Flipflop, pin 9 of IC8 is a pulse occurring every two lines and is combined with the CLMP_P pulses in the AND-gate of IC9 (pins 1,2,13) The output pulse at pin 12 is further used as follows :

* is inverted with IC4 (pins 1 - 2) and shaped with a push-pull Q3/Q4 to make a BRT_P2 pulse (with constant amplitude) for the brightness controls in the driver stages.

* to switch on the FET Q10. This switcher transfers the DC level of the OPAMP-buffer IC6 pins 12,13 and 14, to the bases of the push-pull Q8/Q9. This DC-voltage is the brightness voltage delivered by a dig. pot. of IC1 and variable from 1 to 3 volts.

As a result, we get a pulse BRT_P1 with the same width of the BRT_P2 pulse, but, with a variable amplitude, depending on the brightness adjustment. This pulse is used in the brightness control circuit of the driver circuits. Both pulses, have opposite polarity and are used to install the brightness pulse. The amplitude of this installed pulse will then be analysed in the video end stages by means of the + / - BRT pulses. * the video end stages require two opposite phase pulses - BRT and + BRT. These pulses are generated by two push-pull circuits (Q12/Q13 and Q14/Q15) to improve the rise and fall times and provide sufficient current for the clampers on the end stages.

Note :

During the "ABL control and measurement " time (20 μ S), at the end of the vertical flyback time, no brightness control may be done. Therefore, VP pulses are applied to the IC9 AND inputs with D13/D14.

C. Generation of the pulses for Contrast control: CONTR_PBL, CONTR_P, CONTR_SP, PED_CL., ABL_PN

In between two brightness pulses, alternatively a PED_CL pulse and a contrast pulse are generated. The second RS-Flipflop in IC8 divides once again the output pin 9 by 2. The complementary (opposite phased) outputs are applied respectively to pin 9 and 5 of two AND gates of IC9.

The signals at the pins 3, 4 and 5 are multiplied (AND function) and the corresponding output pin 6 (let's say the "CONTR " pulses in the further explanations) is now used to produce three coincident pulses with different widths.

CONTR_PBL (widest pulse): CONTRast Pulse BLank.

This CONTR- pulse is a bit delayed with the AND gate IC10, pins 1+2, 3, and inverted with Q2.

CONTR_P: (narrower than the above CONTR_PBL pulse)

The original CONTR -pulse is now multiplied with the delayed CONTR_PBL to obtain a smaller pulse, which is now shaped with a Schmitt trigger in IC3 (pins 9 to 8).
CONTR_SP :

The original pulse CONTR is multiplied with the former one and another smaller pulse is created at pin 11 of the AND gate of IC10.

These three pulses, together with the PED_CL pulse, are used in the " contrast control loop" which will be discussed further on in the text.

PED_CL :

The contrast loop needs a "black level" pulse to "check " the value of the black at the moment no contrast pulse nor brightness pulse is inserted.
This PED_CL pulse is generated with the correct DC level by Z8 / Q6 / Q7.

ABL_PN :

There may be no contrast measurement during the ABL time(as ther may be no brightness control neither). Therefore, the video is blocked at the input of the driver (see later) and secondly the contrast pulses are inhibited. The VP pulses are inverted with the Schmitt trigger 1 _> 2 and used as ABL_PN in the video drive stages.

These negative pulses keep pin 3 of IC9 low via D5 to inhibit the contrast pulses.

D. Video drive stages.

It's obvious that the three video drive circuits are more or less identical.
We'll limit the explanations to the blue driver stage.

a) DC Restoration at the input :

The blue video signal is AC coupled to the base of Q320 with C302 and further to the base of Q301 on condition Q315 os "on". The DC level or bias of Q320 and thus of Q301 is amongst others determined by the collector current of Q321. This current is on its turn deterrmined by the base-emitter voltage. This is the voltage across the "hold" capacitor C305.

The moment the CP_INP pulse is applied, the differential amplifier Q323 compares the instant video black level with the reference voltage installed by Z301 at the other base. The " hold "capacitor is charged up via the diff. amplifier or discharged through Q322 in order to keep the voltage stable. The moment the "ABL control measurement" is done , Q315 is blocked and the reference level of Z301 is applied to the base of Q301 to implement the black level.

Later on, in the output stages, the cathode current for this level will be measured and the result used to adapt (let's say stabilize) the brightness level of that colour (see ABL of the output stages).

c) Contrast control loop :

The gain of the video signal (=contrast control) is obtained by a current-sharing gain control stage composed of two transistors in IC301 (3246E). The input current (emitter load) is shared between two outputs, depending on the DC control voltage, which is the difference between the two base voltages.

One of these bases is fixed at a reference of 5.1V and decoupled with C309 (= common base configuration), the other base has a voltage which is the result of the transistor-buffer in the same IC (pin 7 of IC301). This DC-voltage is obtained by the "contrast control loop" circuit (sample and hold circuit) as follows:

The negative CONTR_P pulse forward biases D305 and implements a (common, and fixed) emitter current set by R315. At that moment the current is shared between the two transistors.

The collector current through R317 is determined by its base voltage. (Note that this pulse CONTR_P occurs every four lines)

The video across the collector resistor R317 is capacitively coupled with C312 to the sample and hold circuit.

The sampling contrast pulse CONTR_SP (SamPling, smallest of the three contrast pulses) drives the FET Q304 into conduction and inserts a (reference) contrast pulse of zero volts in the video signal. This reference contrast pulse undergoes now the same game of the video signal during the next lines. Two lines later, when there is no brightness nor contrast pulse, the value of this contrast pulse is sampled (checked) with a PED_CL pulse (PED = pedestal pulse).

The PED_CL (PEDestal CLamp) pulse drives the FET Q305 into conduction and applies (transfers) the amplitude of the contrast pulse to the "hold" capacitor C314. This contrast level will depend on the gain of the former differential amplifier in IC301 as discussed.

The DC voltage across the capacitor C314 is now compared with the required CONTRAST voltage from a dig.pot. in IC1(see sheet 4).

Any voltage difference will now be regulated by the feedback system until both voltages on the comparator, pins 12 and 13, are the same. The output of the comparator, pin 14, determines the gain of the video as described earlier.

E. Software controlled gain of the red and blue video signals.

A contrast offset of the red and blue signals can be adjusted by the user (white balance control) , thus, via the software controls.

A variable voltage "Gain R" and "Gain B" is received from two digital potentiometers in IC2 on the subunit (sheet 4). We limit the explanation again to the blue gain control stage.

Two differential amplifiers of IC303 are used for this purpose. The first dif. amp. is a current sharing amplifier and performs the real gain. The second one generates the difference voltage and the obtained voltage is mirrored to the bases of the first one. The collector current of the second DIF AMP can be increased or decreased by "adding" or "deviating" collector current via R337.

Note that the base voltage of the other transistor(s) is fixed with Z304 at 6.8 volts.

As the bases of the two DIF AMP's are coupled, the base differential voltage of the second one is mirrored to the first DIF AMP. The current sharing between the two transistors of the DIF AMP determines the gain (=offset) control of the video signal.

F. Brightness control (insertion of the brightness pulse):

The brightness pulse BRT_P1 (with a variable amplitude) is applied, to the common emitter of the differential amplifier. The negative pulse with constant amplitude (= negative offset) is applied at the base of Q306, an emitterfollower. This negative offset is required to install the brightness pulse always with the same polarity, with respect to the black level of the video.

At the emitterfollower Q306 the brightness pulse has an amplitude determined by the amplitude of the BRT_P1 pulse and the signal is now coupled to the amplifier Q307 via R350 and P351. Potentiometer P351 allows to adjust the amplitude and is consequently a gain control.

The contrast pulses, inserted at the contrast control loop are now "removed" with the CONTR_PBL pulses via R353 and D308.

From this point, the videosignal is DC coupled up to the drive of the crt's, therefore, the DC level stabilisation is very important. The next circuit stabilizes the DC level of the video. This is performed with a feedback system checking the black level at the moment no brightness nor contrast pulse is present. The pulse utilized to accomplish this is the PED_CL pulse via D309 to the emitter of Q313.

The videosignal is applied to a comparator Q312 / Q313. Note that VPED_OUT is the reference voltage of +1.45 volt, applied to the base of Q313.

At the moment a PED_CL pulse is applied to D309, the latter is blocked and the emitter of Q313 is set at $1.45 + 0.6 = 2$ volts and then a comparison between the voltages on the bases of Q313 and Q312 occurs. There are two possibilities :

* The instant black level at the base of Q312 is lower than 1.45 volts.
In this case Q312 is forward biased turning on Q314. Q314 discharges C345 (connected to base of Q310) to decrease its voltage.

*The instant black level at the base of Q312 is higher than 1.45 volts. In this case Q314 is biased off and Q313 charges C345, increasing its voltage.

The voltage across the capacitor C345 is transferred to the base of Q306 with the Darlington Q309/Q310 and determines (=stabilizes) the DC level of the videosignal. The high input impedance of the Darlington assures a low current drain on C345 as this measurement only happens every four lines.

G. Black level control of red and blue :

The BLKLV_B voltage (1 - 3 volts) from IC2 is buffered by IC7 pins 12, 13 and 14 and sent to the base of Q318. At the moment a BRT_P2 pulse blocks D310. The collector voltage of Q318 is implemented via a voltage adaptation (Z305) to the collector of Q317 and provides an offset of the brightness pulse.

Q319 delivers the blue video on a 75 Ohm load to the output stage.

Technical description of the "QUAD DECODER"

Introduction.

This Quad decoder subunit uses the set TDA4650 / TDA4660 / TDA4670 for luminance and chrominance processing.

The TDA4650 chip is an improved version of the former TDA 4557. The external components are reduced to a minimum, the alignments are limited by using the electronically tuned delay lines of the TDA4660.

The TDA4660 comprises two horizontal sync locked CCD (Charged Coupled Devices) delay lines working in NTSC as a 'comb filter', as a propagation delay line in PAL and as simple delay line in SECAM.

Cross colour is minimised in SECAM as at the outputs of the colour chip TDA4650 only one colour difference signal at a time is available.

The TDA4670 comprises the luminance delay line, the enhancing or sharpness control and improves the transient of the colour signals. This chip is I2C-bus controlled.

Three connectors realise the connection between the main "RGB - driver" board and this decoder (subunit) board.

A. Video composite.

The video composite arrives at the buffer Q406 where it is AC coupled and available at the emitter on a correct DC level for the switching emitterfollowers.

Luminance flow :

The colour subcarrier is rejected with L404 / C421 for Pal / SECAM and N4 or with L403 / C422, tuned at 3.58 Mhz for NTSC 3.58.

Note that both transistors Q503 and Q502 are blocked in SVideo.

A next buffer Q417 supplies the Yin input of the TDA4670 through the capacitor C468.

In the first stage of this IC the signal is clamped and then reduced to a suitable level for the following delay line. This delay line consists of 13 all-pass cells (tcell = 90 nS) which are built up with gyrators.

This delay time is switchable via the I2C bus. Via software the correct delays for Pal/ Secam and NTSC 3.58Mhz are loaded. A peaking (sharpness control) is incorporated with two selectable centre frequencies (2.6 and 5 Mhz, not available here). This circuit consists of two additional delay cells, two 0.5 gain inverting amplifiers and an adding stage.

For better noise reduction the peaking signal is applied via a coring stage and a switchable amplifier before being added to the main signal. The grade of peaking (enhancing) can be controlled via the I2C bus.

Chrominance :

The chrominance is filtered by one of the chrominance filters Pal / N4 or Secam and applied via C441 to pin 15 of the colour chip TDA4650. In case of N3 the filter L402 is tuned to 3.58 Mhz with C427 .

The IC identifies and demodulates PAL, SECAM, NTSC 3.58 and NTSC 4.43

chrominance signals. The automatic standard recognition, by sequential inquiry of the backporch information, is improved by allowing only Secam when it is 50hz.

The system identification part scans during 4 vertical periods the colour in the sequence Pal / Secam / N3 / N4. Four switched outputs for chrominance filter selection are available at the pins 28 - 25. Note that, by applying an external +9 volts to the appropriated pin the chip can be forced into one colour system.

a) The delay line chip TDA4660 :

The delay line chip consists of :

a) two delay lines of 64 μ S based upon a switched capacitor technique (CCD). These have normal delay function for PAL and SECAM, and a comb filter function for NTSC to avoid chrominance cross colour.

b) An internal clock of 3Mhz for the shift register delay lines, derived from a 6Mhz VCO, which is locked to the line frequency with a sandcastle pulse at pin 5. This means that the delay time is automatically adjusted, and no alignment is necessary. A divider by 192 provides a clamp pulse for the input circuits. Sample and hold low-pass filters behind the delay lines suppress the clock signal. The delayed and un-delayed signals are added and fed to the output pins 11, 12 via a buffer stage.

By adding two subsequent lines in SECAM, and as one of these lines is blanked (line sequential blanking), we get at any time the two colour difference signals at the respective outputs.

b) SECAM .

For SECAM an amplitude limiting circuit is active before quadratic modulation. The signal amplitude and H/2 content of the colour difference signals (R-Y) and (B-Y) is dependent on the characteristics of the external tuned circuit at pins 7, 8, 9 and 10.

The resonance frequency of this external circuit is adjusted in such way that the demodulated Fo - carrier is zero for the (B-Y), with L406.

Now, by adjusting the quality factor of the circuit with P400, it is possible to get zero output for the (R-Y) Fo carrier (un modulated carrier).

De-emphasis is applied with internal resistors and tuned to the correct value with the capacitors at pins 4 and 2 (C453 and C452).

The colour difference signals are line sequentially (each two lines) blanked to a level equal to the black signal (black) level.

By this measure only one colour signal is available at any time at the output of the colour decoder IC in order to reduce colour crosstalk.

c) PAL / NTSC .

The 8Mhz x-tal is connected at pin 21 and tuned with C436, the 7.2 Mhz x-tal for NTSC 3.58 Mhz is connected at pin 19 and tuned with C435. They can be adjusted to the centre frequency "fo" when the service pin 17 is below the 0.5 volts level (burst off).

The PAL switching stage (correction of the [R-Y] polarity) is disabled in NTSC and on the other hand the phase shifting or Hue is disabled for PAL.

With the delay lines the delayed and un-delayed signals are added to compensate propagation delays in PAL, or in other words to obtain the correct colours (but a bit desaturated).

In NTSC, adding delayed and un-delayed signals means that the luminance is suppressed.

The IC TDA4661 here behave as a 'comb' filter for NTSC.

d) CTI / Saturation control / matrixing .

The (R-Y) and (B-Y) signals are now sent to the CTI chip TDA4670 for improving the transients of these chroma signals. This is performed as follows .

Both colour difference channels consist of a clamping stage at the input, a buffer amplifier, a storage stage and an output amplifier. The storage, which is operated by a differential amplifier, stores the colour difference signal during the transient time of the input signal and then switches rapidly to accept the new signal.

A signal formed by differentiation, full wave rectifying and summing the two colour difference signals is compared with a reference signal. The resultant signal is used to switch the sample and hold circuits.

Note that the colour transient improvement function can be switched off or on by means of the I²C-bus commands (not implemented here).

The colour difference signals are capacitively coupled to IC410 which consists of four I²C controlled potentiometers. Two of these are utilised for the saturation control and one is used for the hue control.

The outputs , -(B-Y) and -(R-Y) are now matrixed with the correct ratio to obtain -(G-Y) and further the addition of Y provides Red, Green and Blue. These RGB signals leave the decoder via a current driver transistor to reach the "RGB INP SW" module.

B. SVideo .

The +SVid voltage forward biases Q404 and Q416 to respectively "accept" the chrominance and luminance signals. This voltage automatically blocks Q405 and Q414 / Q415.

The flow is now the same as described before.

Q407's base does not receive a base voltage when an RGB mode is selected in order to block the current driver outputs for Red, Green and Blue.

ADJUSTMENT PROCEDURE RGB DRIVER+QUAD DECODER 76 21175

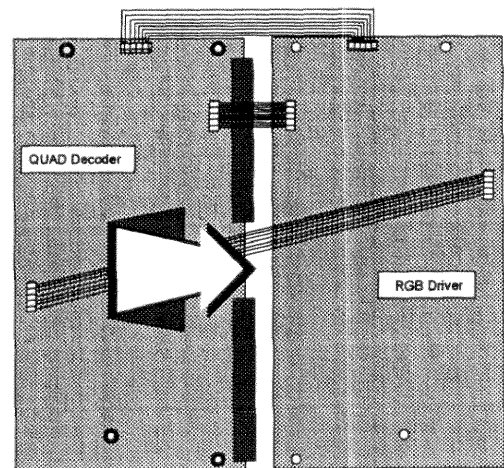
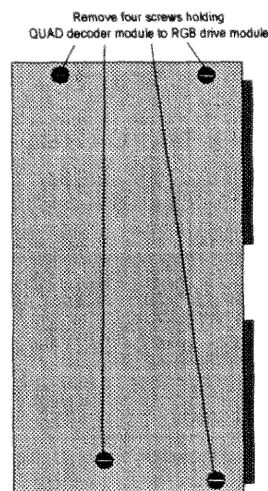
The alignment of the RGB Driver+QUAD Decoder is restricted to the adjustment of :

- alignment of the QUAD decoder module (sub board)
- alignment of the RGB DRIVER module (main board)

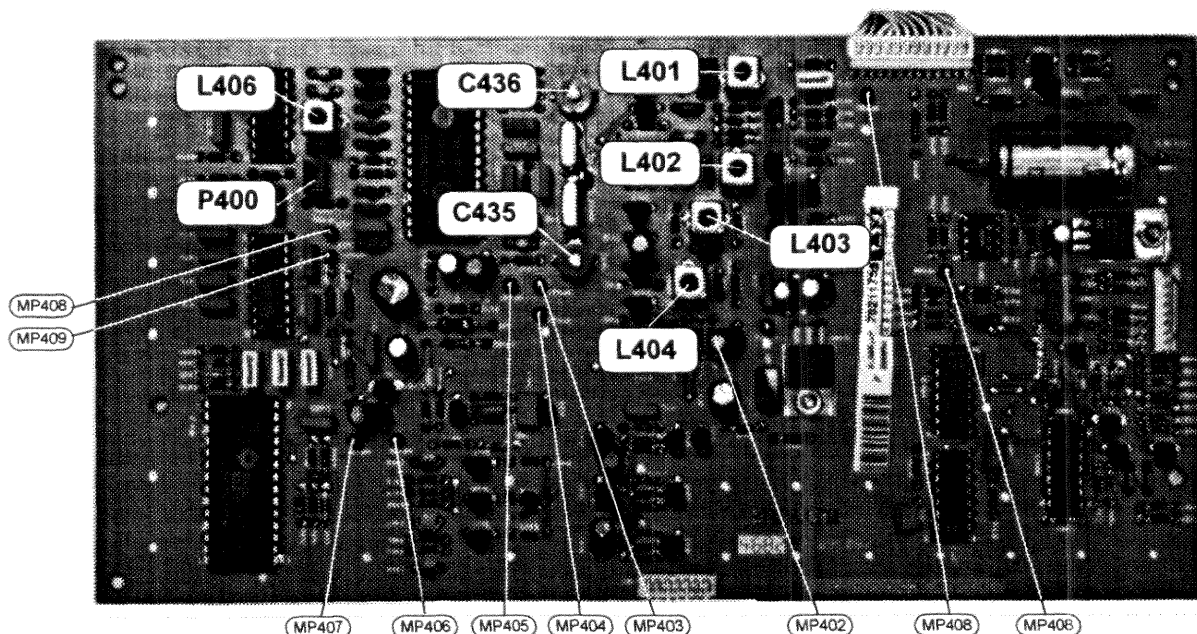
Alignment of the QUAD decoder 76 21175S (sub board)

Preparation

- Put the module on the extension boards. Separate the decoder module from the RGB Drive module (removing 4 screws and pulling out two connectors). Rotate the decoder module and reinstall the cable connections, using the extension cables.



Location of controls and measurement points (MP)



- Switch the projector in the VIDEO MODE. Select source 1.
- Connect to the VIDEO input e.g. an electronic colour test video signal (see photo)

A. VIDEO INPUT SIGNAL PAL COLOUR TEST IMAGE

1. REFERENCE OSCILLATOR (C436)

- if there is no colour, adjust trimming capacitor C436 until colour is being received.
- short-circuit pin 17 of IC406 to earth (MP405).
- adjust the trimming capacitor C436 for colour zero beat.
- remove the short-circuit.

2. CHROMA REJECTOR (L404) (photo 1)

- connect an oscilloscope to the MP406 (Y Out IC408).
- adjust the core of coil L404 for a minimum of chroma in the video signal.

3. CHROMA FILTER (L402) (photo 2)

- connect the oscilloscope to the MP403 (CRI pin 15 IC406).
- adjust the core of coil L402 for maximum chromasignal.

B. VIDEO INPUT SIGNAL SECAM COLOUR TEST IMAGE

4. SECAM REFERENCE CIRCUIT (L406 - P400) (photo 3 & 4)

- connect the oscilloscope to the MP409 (pin 7 of IC408).
- adjust L406 so that the level of the (B-Y) signal without colour information is the same as the level during blanking.
- connect the oscilloscope to the MP408 (pin 3 of IC408).
- adjust P400 so that the level of the (R-Y) signal without colour information is the same as the level during blanking.
- If necessary the level in (B-Y) channel has to readjust to zero with the coil.

5. BELL FILTER (L401) (photo 5)

- connect an oscilloscope to the MP403 (CRI pin 15 IC406).
- adjust L401 for a flat amplitude of the signal during two successive lines.

C. VIDEO INPUT SIGNAL NTSC 3.58 TEST IMAGE

6. REFERENCE OSCILLATOR (C435)

- If there is no colour, adjust trimming capacitor C435 until colour is being received.
- short circuit pin 17 of IC406 to earth (MP405).
- adjust trimming capacitor C435 for a colour zero beat.
- remove the short-circuit.

17. CHROMA REJECTOR (L403) (refer to photo 1)

- connect the oscilloscope to the MP406 (Y Out IC408).
- adjust the core of coil L403 for a minimum of chroma in the video signal.

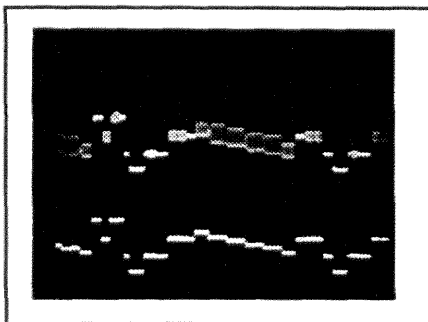
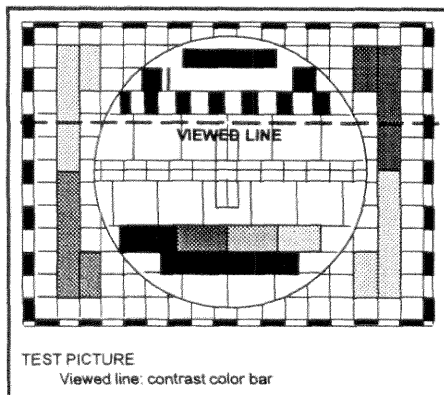


Foto 1
Alignment chroma rejector L504
Upper track: viewed video line
Lower track: Y signal

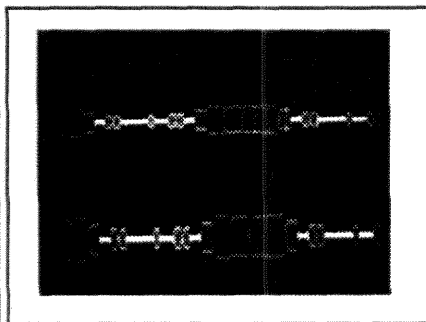


Foto 2
Alignment maximum Chroma L500
Upper track: viewed video line
Lower track: max Chroma signal

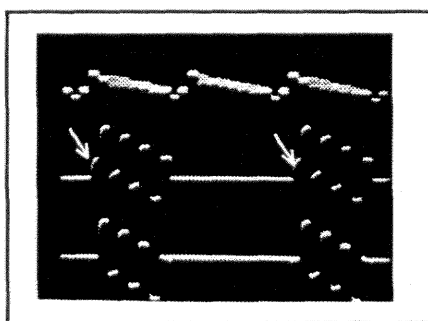


Foto 3
Alignment SECAM Ref circuit L502 (B-Y)
Upper track: viewed video line
Lower track:
1: incorrect setting
2: correct setting

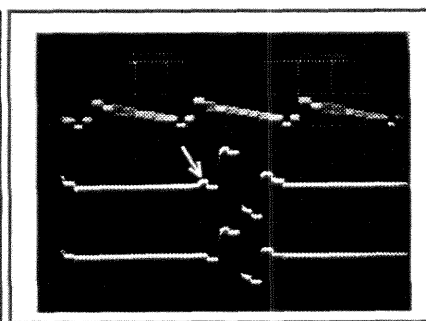


Foto 4
Alignment SECAM Ref circuit P500 (R-Y)
Upper track: viewed video line
Lower track:
1: incorrect setting
2: correct setting

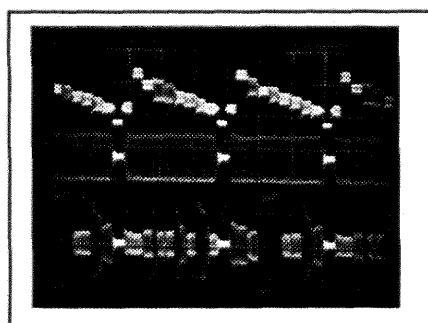
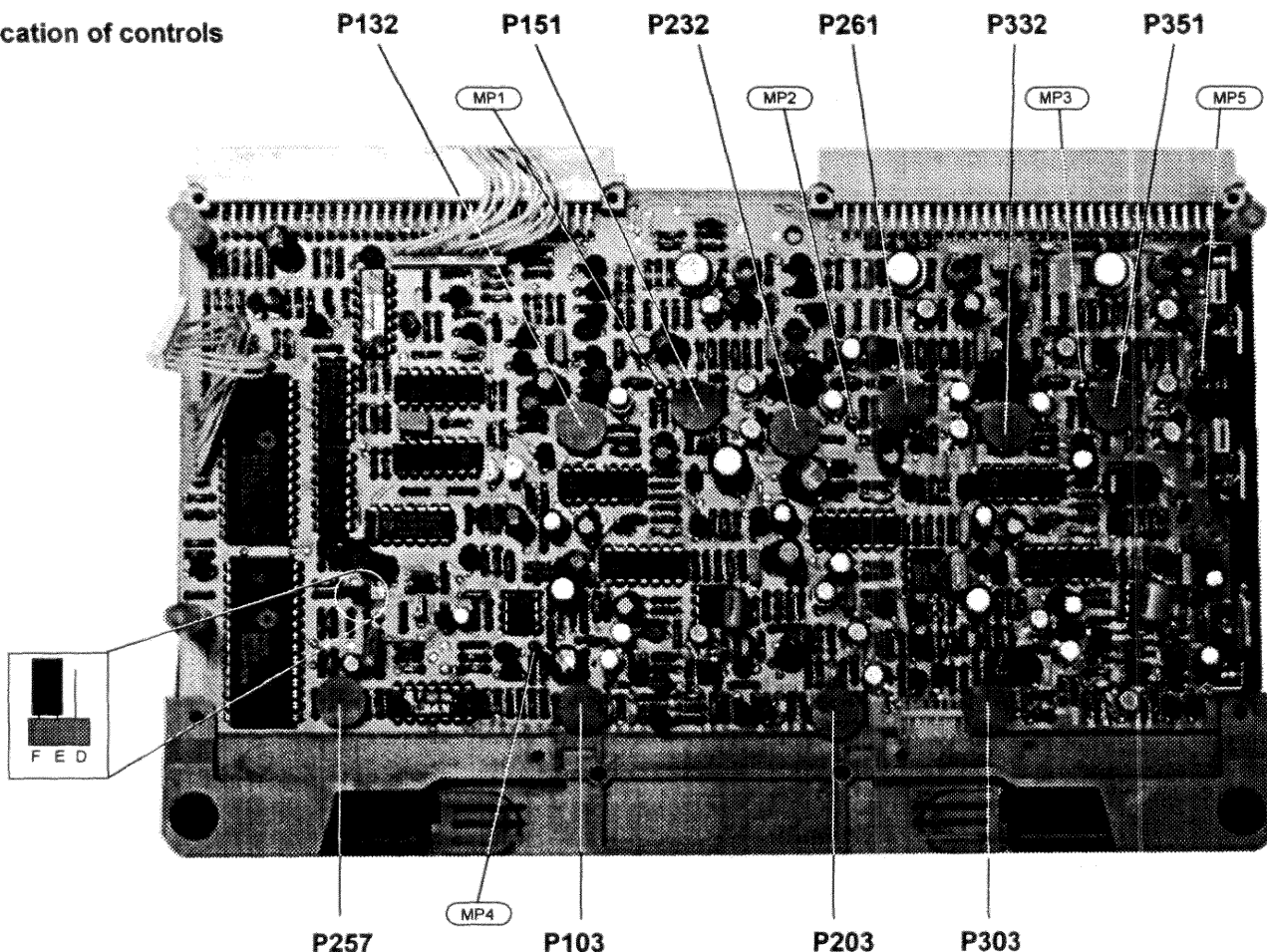


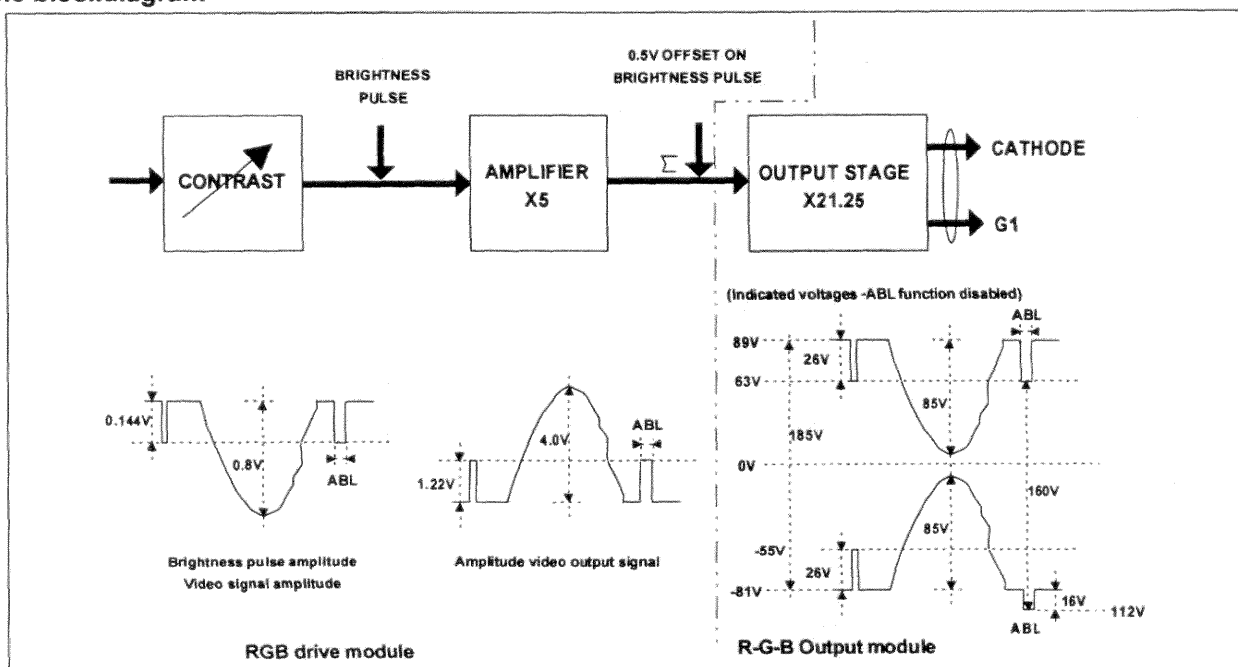
Foto 5
BELL FILTER
Upper track: viewed video line
Lower track:
1: correct setting
2: incorrect setting

Location of the adjustments controls on the Quad decoder and the RGB drive

Location of controls



Principle blockdiagram



Alignment of the RGB Driver 76 21175 (sub module)

Preparation

- Put the module on the extension boards. Separate the decoder module from the RGB Drive module (removing 4 screws and pulling out two connectors). Rotate the decoder module and reinstall the cable connections, using the extension cables. (refer to illustration added for the decoder adjustment)
- Provide a RGB externally generated source with adjustable amplitude to the projector.
- Using the RCU (Remote Control unit), adjust the Brightness to 50% on the bar scale, i.e. mid-position, the Contrast and Red/Blue gain to 99%, i.e. maximum. (refer to installation manual)

Adjustment

Adjustment of P257

- Connect an oscilloscope to the testpoint TP4.
- The signal on the scope must be a horizontal straight line (only DC level). If a pulse appears on the DC level, readjust P257 until there is no pulse.

Important:

The adjustment procedure below requires an amplitude of the R, G and B signal on the input connector J5 of 0.7Vpp .

Important: this voltage must be measured as follows,

- remove connector J5 on the Input RGB-Analog+Switching module 76 17481.
- terminate the R, G and B output with a resistor of 75 Ohm
- adjust input voltage for 0.7Vpp on the 75 Ohm resistors
- remove the resistors and plug in connector J5

Brightness pulse amplitude	P132	red channel
	P232	green channel
	P332	blue channel

Connect the oscilloscope successively to the testpoints TP1, TP2 and TP3 (the base of transistor Q106, Q206 and Q306) and adjust the corresponding potentiometer for a brightness pulse amplitude of 0.144Vpp.

Video signal amplitude	P103	red channel
	P203	green channel
	P303	blue channel

Connect the oscilloscope successively to the testpoints TP1, TP2 and TP3 (the base of transistor Q106, Q206 and Q306) and adjust the corresponding potentiometer for a video amplitude of 0.8Vpp.

Amplitude video output signals	P151	red channel
	P251	green channel
	P351	blue channel**

Connect the oscilloscope successively to contact A1-C1, A9-C9 and A25-C25 of the module connector J1 and adjust the corresponding potentiometer for a video amplitude of 4.0Vpp.

****Adjustment of P351 Blue channel:**

When the components R380, R379 and D314 are mounted on the board (actual version) , potentiometer P351 has to be adjusted for a video amplitude of 4.0Vpp, but for an input signal of 0.46Vpp.

This input voltage must be measured as follows,

- remove connector J5 on the Input RGB-Analog+Switching module 76 17481.
- terminate the B output with a resistor of 75 Ohm
- adjust input voltage for 0.46Vpp on the 75 Ohm resistor
- remove the resistor and plug in connector J5

Parts listing R7621175CPL

SIT.	ITEM NO.	DESCRIPTION	QUANTITY	SIT.	ITEM NO.	DESCRIPTION	QUANTITY
40	R133014	Q ACC MNTG PAD TO18	12	C124	R111678	C EL BRA 10M M 25E2 85	1
110	R1330291	Q ACC ISO MICA TO220	1	C125	R112774	C CE MI 100N S 63E2	
111	R1330292	Q ACC ISO BSHG TO220	1	C126	R111510	C EL RA 22M M 25E2 85	
				C127	R111548	C EL RA 2M2M 50E2 85	1
10	R302108	CORE TUBE 3.5 /1.3 X 3	9	C129	R112774	C CE MI 100N S 63E2	1
20	R302108	CORE TUBE 3.5 /1.3 X 3	1	C139	R112428	C NPO MI 6P8C 63E1	1
30	R302108	CORE TUBE 3.5 /1.3 X 3	3	C140	R111478	C EL RA 220M M 25E2 85	1
				C141	R112422	C NPO MI 2P2C 63E1	1
50	R313729	J PIN TESTEYE	5	C142	R111500	C EL RA 47M M 10E2 85	
				C143	R112774	C CE MI 100N S 63E2	
121	R3620226	SCR D84 M 3 X 8 SI	3	C144	R111510	C EL RA 22M M 25E2 85	
120	R3661026	NUT D934 M 3 I	3	C145	R113720	C POMERA 47N K 63E2	
122	R367502	WSHR D6798 A 3.2 S Z	3	C146	R112774	C CE MI 100N S 63E2	
				C147	R112774	C CE MI 100N S 63E2	
500	R722276	LOCK PJ49 PCB UN CPL	1	C148	R111510	C EL RA 22M M 25E2 85	
	R7621175S	UN RGB PJ51 G1200 DEC MK2	1	C151	R112774	C CE MI 100N S 63E2	
				C153	P210122	C#X7R MU 100N K 50 1206	1
100	R802629	HTSNK PJ49 RGB PR_AMP	1	C155	P210122	C#X7R MU 100N K 50 1206	1
101	R802692	HTSNK PJ49 FIX HTSNK	2	C156	P210122	C#X7R MU 100N K 50 1206	1
10	R803602	SPR L28 D 6 M 3 P	4	C157	P210122	C#X7R MU 100N K 50 1206	1
C 1	R112774	C CE MI 100N S 63E2		C158	P210138	C#COG MU 10P J 50 1206	1
C 2	R112774	C CE MI 100N S 63E2		C201	R112731	C CE MI 220P K100E2	1
C 3	R111511	C EL RA 33M M 16E2 85	1	C202	R112739	C CE MI 1N K100E2	1
C 4	R113729	C POMERA 270N K 63E2		C203	R112233	C NPO MI 18P G 63E2	
C 5	R111546	C EL RA 1M M 50E2 85		C204	R111466	C EL RA 100M Z 16E2 85	
C 6	R112774	C CE MI 100N S 63E2		C205	R112774	C CE MI 100N S 63E2	
C 7	R112774	C CE MI 100N S 63E2		C206	R112774	C CE MI 100N S 63E2	1
C 8	R112774	C CE MI 100N S 63E2		C207	R112230	C NPO MI 10P G 63E2	1
C 9	R111500	C EL RA 47M M 10E2 85		C208	R112774	C CE MI 100N S 63E2	
C 10	R1159101	C PP RA 560P J100E2		C209	R111510	C EL RA 22M M 25E2 85	
C 11	R111511	C EL RA 33M M 16E2 85		C210	R111678	C EL BRA 10M M 25E2 85	1
C 12	R113730	C POMERA 330N K 63E2		C211	R112365	C N750MI 180P J 63E2	
C 13	R113730	C POMERA 330N K 63E2		C212	R112739	C CE MI 1N K100E2	
C 14	R111548	C EL RA 2M2M 50E2 85		C213	R112774	C CE MI 100N S 63E2	
C 15	R112774	C CE MI 100N S 63E2		C214	R113724	C POMERA 100N K 63E2	
C 16	R112774	C CE MI 100N S 63E2		C215	R113732	C POMERA 470N K 63E2	1
C 17	R111548	C EL RA 2M2M 50E2 85	1	C220	R111465	C EL RA 47M M 16E2 85	
C 18	R111477	C EL RA 100M Z 25E2 85		C226	R111510	C EL RA 22M M 25E2 85	
C 19	R112368	C N750MI 330P J 63E2		C227	R111548	C EL RA 2M2M 50E2 85	
C 20	R111510	C EL RA 22M M 25E2 85		C229	R112774	C CE MI 100N S 63E2	1
C 21	R112238	C NPO MI 47P G 63E2		C239	R112428	C NPO MI 6P8C 63E1	1
C 22	R112235	C NPO MI 27P G 63E2		C240	R111478	C EL RA 220M M 25E2 85	1
C 23	R112242	C NPO MI 100P J 63E2		C241	R112400	C P100MI 0P5C 63E1	1
C 24	R112741	C CE MI 1N5K100E2		C242	R111500	C EL RA 47M M 10E2 85	
C 25	R1115468	C EL7 RA 1M M 50E1 85	1	C243	R112774	C CE MI 100N S 63E2	
C 26	R1115468	C EL7 RA 1M M 50E1 85	1	C244	R111510	C EL RA 22M M 25E2 85	
C101	R112731	C CE MI 220P K100E2	1	C245	R113720	C POMERA 47N K 63E2	
C102	R112739	C CE MI 1N K100E2	1	C246	R112774	C CE MI 100N S 63E2	
C103	R112205	C P100MI 1P5C 63E2	1	C247	R112774	C CE MI 100N S 63E2	
C104	R111466	C EL RA 100M Z 16E2 85		C248	R111510	C EL RA 22M M 25E2 85	
C105	R112774	C CE MI 100N S 63E2		C251	R112774	C CE MI 100N S 63E2	
C106	R112774	C CE MI 100N S 63E2	1	C252	R112229	C NPO MI 8P2C 63E2	1
C107	R112230	C NPO MI 10P G 63E2	1	C301	R112731	C CE MI 220P K100E2	1
C108	R112774	C CE MI 100N S 63E2		C302	R112739	C CE MI 1N K100E2	1
C109	R111510	C EL RA 22M M 25E2 85	1	C303	R112231	C NPO MI 12P G 63E2	1
C110	R111678	C EL BRA 10M M 25E2 85	1	C304	R111466	C EL RA 100M Z 16E2 85	
C111	R112365	C N750MI 180P J 63E2		C305	R112774	C CE MI 100N S 63E2	1
C112	R112739	C CE MI 1N K100E2		C306	R112774	C CE MI 100N S 63E2	1
C113	R112774	C CE MI 100N S 63E2		C307	R112230	C NPO MI 10P G 63E2	1
C114	R113724	C POMERA 100N K 63E2	1	C308	R112774	C CE MI 100N S 63E2	
C115	R113732	C POMERA 470N K 63E2	1	C309	R111510	C EL RA 22M M 25E2 85	
C120	R111465	C EL RA 47M M 16E2 85		C310	R111678	C EL BRA 10M M 25E2 85	1
C121	R112774	C CE MI 100N S 63E2		C311	R112365	C N750MI 180P J 63E2	
C123	R111510	C EL RA 22M M 25E2 85		C312	R112739	C CE MI 1N K100E2	
				C313	R112774	C CE MI 100N S 63E2	

RGB Driver+QUAD Decoder

R7621175

Sub module: Quad decoder R7621175S

C314	R113724	C POMERA 100N K 63E2		D205	R1316361	D Y BAT85	030200 DO35	
C315	R113732	C POMERA 470N K 63E2	1	D206	R1316361	D Y BAT85	030200 DO35	
C320	R111465	C EL RA 47M M 16E2 85		D207	R131621	D S 1N4148	075150 DO35	
C321	R112774	C CE MI 100N S 63E2		D208	R131628	D S BAW62	075200 DO35	1
C323	R111510	C EL RA 22M M 25E2 85		D209	R131621	D S 1N4148	075150 DO35	
C324	R111678	C EL BRA 10M M 25E2 85	1	D210	R131621	D S 1N4148	075150 DO35	
C325	R112774	C CE MI 100N S 63E2		D211	R131621	D S 1N4148	075150 DO35	
C326	R111510	C EL RA 22M M 25E2 85		D212	R131621	D S 1N4148	075150 DO35	
C327	R111548	C EL RA 2M2M 50E2 85		D213	R131621	D S 1N4148	075150 DO35	
C329	R112774	C CE MI 100N S 63E2	1	D215	R131628	D S BAW62	075200 DO35	1
C339	R112428	C NPO MI 6P8C 63E1	1	D301	R131623	D S BA243	020100 DO35	1
C340	R111478	C EL RA 220M M 25E2 85	1	D302	R131826	D V BB112	008 SOD69	1
C341	R112403	C P100MI 1P C 63E1	1	D305	R1316361	D Y BAT85	030200 DO35	
C342	R111500	C EL RA 47M M 10E2 85		D306	R1316361	D Y BAT85	030200 DO35	
C343	R112774	C CE MI 100N S 63E2		D307	R131621	D S 1N4148	075150 DO35	
C344	R111510	C EL RA 22M M 25E2 85		D308	R131628	D S BAW62	075200 DO35	1
C345	R113720	C POMERA 47N K 63E2		D309	R131621	D S 1N4148	075150 DO35	
C346	R112774	C CE MI 100N S 63E2		D310	R131621	D S 1N4148	075150 DO35	
C347	R112774	C CE MI 100N S 63E2		D311	R131621	D S 1N4148	075150 DO35	
C348	R111510	C EL RA 22M M 25E2 85		D312	R131621	D S 1N4148	075150 DO35	
C351	R112774	C CE MI 100N S 63E2		D313	R131621	D S 1N4148	075150 DO35	
C352	R112231	C NPO MI 12P G 63E2		D314	R131635	D Y 5082-2800		1
C353	P210122	C# X7R MU 100N K 50 1206	1	D315	R131628	D S BAW62	075200 DO35	1
C358	P210138	C# COG MU 10P J 50 1206	1					
D 1	R131621	D S 1N4148	075150 DO35	I 1	R132833	U 76013 SC	DIP28 P	1
D 2	R131621	D S 1N4148	075150 DO35	I 2	R132833	U 76013 SC	DIP28 P	1
D 3	R131621	D S 1N4148	075150 DO35	I 3	R137551	U 74HCT14	DIP14 P	1
D 4	R131621	D S 1N4148	075150 DO35	I 4	R137507	U 7406	DIP14 P	1
D 5	R131621	D S 1N4148	075150 DO35	I 5	R134104	U 741 LM	DIP8 P	1
D 6	R131621	D S 1N4148	075150 DO35	I 6	R134113	U 084 TL	DIP14 P	1
D 7	R131621	D S 1N4148	075150 DO35	I 7	R134113	U 084 TL	DIP14 P	1
D 8	R1316361	D Y BAT85	030200 DO35	I 8	R137548	U 74HCT74	DIP14 P	1
D 9	R131621	D S 1N4148	075150 DO35	I 9	R137010	U 74HCT11	DIP14 P	1
D 10	R131621	D S 1N4148	075150 DO35	I 10	R137537	U 74HCT08	DIP14 P	1
D 11	R1316361	D Y BAT85	030200 DO35	I 11	R134001	U 7805	TO220 P	1
D 13	R1316361	D Y BAT85	030200 DO35	I 12	R134002	U 7812	TO220 P	1
D 14	R1316361	D Y BAT85	030200 DO35	I 13	R134016	U 7912	TO220 P	1
D 15	R131621	D S 1N4148	075150 DO35	I101	R132705	U 3246E CA	DIP14 P	1
D 16	R131621	D S 1N4148	075150 DO35	I102	R134124	U 082 TL	DIP8 P	1
D 17	R131621	D S 1N4148	075150 DO35	I103	R132705	U 3246E CA	DIP14 P	1
D 18	R131621	D S 1N4148	075150 DO35	I201	R132705	U 3246E CA	DIP14 P	1
D 19	R131621	D S 1N4148	075150 DO35	I202	R134124	U 082 TL	DIP8 P	1
D 21	R131621	D S 1N4148	075150 DO35	I301	R132705	U 3246E CA	DIP14 P	1
D 22	R131621	D S 1N4148	075150 DO35	I302	R134124	U 082 TL	DIP8 P	1
D 24	R131621	D S 1N4148	075150 DO35	I303	R132705	U 3246E CA	DIP14 P	1
D 25	R131621	D S 1N4148	075150 DO35					
D 26	R131621	D S 1N4148	075150 DO35	J1	R313525	J EUR2C MBS P64 E1C2S 1,6		1
D 27	R131621	D S 1N4148	075150 DO35	J2	R313525	J EUR2C MBS P64 E1C2S 1,6		1
D 28	R131621	D S 1N4148	075150 DO35	J5	R313947	J CT H MBS P 7 M2SN		1
D 29	R131621	D S 1N4148	075150 DO35					
D 70	R1316361	D Y BAT85	030200 DO35	L101	R302108	CORE TUBE 3.5 /1.3 X 3		1
D 71	R1316361	D Y BAT85	030200 DO35	L102	R774271	COIL IF N 6.5 B5ZK D0.2		1
D 72	R1316361	D Y BAT85	030200 DO35	L201	R302108	CORE TUBE 3.5 /1.3 X 3		1
D101	R131623	D S BA243	020100 DO35	L202	R774271	COIL IF N 6.5 B5ZK D0.2		1
D102	R131826	D V BB112	008 SOD69	L301	R302108	CORE TUBE 3.5 /1.3 X 3		1
D105	R1316361	D Y BAT85	030200 DO35	L302	R774271	COIL IF N 6.5 B5ZK D0.2		1
D106	R1316361	D Y BAT85	030200 DO35					
D107	R131621	D S 1N4148	075150 DO35	P103	R107003	R TCE H100E M 0W5 S7 TS		1
D108	R131628	D S BAW62	075200 DO35	P132	R107005	R TCE H500E M 0W5 S7 TS		1
D109	R131621	D S 1N4148	075150 DO35	P151	R107004	R TCE H200E M 0W5 S7 TS		1
D110	R131621	D S 1N4148	075150 DO35	P203	R107003	R TCE H100E M 0W5 S7 TS		1
D111	R131621	D S 1N4148	075150 DO35	P232	R107005	R TCE H500E M 0W5 S7 TS		1
D112	R131621	D S 1N4148	075150 DO35	P251	R107004	R TCE H200E M 0W5 S7 TS		1
D113	R131621	D S 1N4148	075150 DO35	P257	R107006	R TCE H 1K M 0W5 S7 TS		1
D115	R131628	D S BAW62	075200 DO35	P303	R107003	R TCE H100E M 0W5 S7 TS		1
D201	R131623	D S BA243	020100 DO35	P332	R107005	R TCE H500E M 0W5 S7 TS		1
D202	R131826	D V BB112	008 SOD69	P351	R107004	R TCE H200E M 0W5 S7 TS		1

RGB Driver+QUAD Decoder

R7621175

Sub module: Quad decoder R7621175S

PC	R780138	PCD PJ51 G1200 RGB DVR	1	Q301	R131491	Q BSX20 .2369 N SS TO18	1
Q 1	R1314295	Q BC549B N SS TO92		Q304	R1314651	Q BF245B FN SS TO92	1
Q 2	R132910	Q BS170 FN SS TO92	1	Q305	R1314651	Q BF245B FN SS TO92	1
Q 3	R1314295	Q BC549B N SS TO92		Q306	R131491	Q BSX20 .2369 N SS TO18	1
Q 4	R1314181	Q BC559B P SS TO92		Q307	R132926	Q BFQ63 N SS TO72	1
Q 5	R1314295	Q BC549B N SS TO92		Q308	R132926	Q BFQ63 N SS TO72	1
Q 6	R1314181	Q BC559B P SS TO92		Q309	R1314182	Q BC559C P SS TO92	
Q 7	R1314182	Q BC559C P SS TO92		Q310	R1314182	Q BC559C P SS TO92	
Q 8	R1314295	Q BC549B N SS TO92		Q311	R132925	Q BFQ53 N SS TO72	1
Q 9	R1314181	Q BC559B P SS TO92		Q312	R1314181	Q BC559B P SS TO92	
Q 10	R1314651	Q BF245B FN SS TO92	1	Q313	R1314181	Q BC559B P SS TO92	
Q 12	R131426	Q BC337 N SS TO92		Q314	R1314295	Q BC549B N SS TO92	
Q 13	R1314181	Q BC559B P SS TO92		Q315	R132910	Q BS170 FN SS TO92	1
Q 14	R1314295	Q BC549B N SS TO92		Q317	R132925	Q BFQ53 N SS TO72	1
Q 15	R1314181	Q BC559B P SS TO92		Q318	R1314295	Q BC549B N SS TO92	
Q 16	R1314295	Q BC549B N SS TO92		Q319	R132911	Q 2N5583 P SS TO39	1
Q 17	R1314181	Q BC559B P SS TO92		Q320	R132954	Q BFY90 N SS TO72	1
Q 20	R1314295	Q BC549B N SS TO92		Q321	R132916	Q BS250 FN SS TO92	1
Q 21	R1314295	Q BC549B N SS TO92	1	Q322	R1314181	Q BC559B P SS TO92	1
Q 22	R1314181	Q BC559B P SS TO92	1	Q323	R132944	Q BCY87 2N SS TO71	1
Q 23	R132910	Q BS170 FN SS TO92	1	Q324	R1314651	Q BF245B FN SS TO92	1
Q101	R131491	Q BSX20 .2369 N SS TO18	1	Q325	R132910	Q BS170 FN SS TO92	1
Q104	R1314651	Q BF245B FN SS TO92	1	Q326	R1314295	Q BC549B N SS TO92	1
Q105	R1314651	Q BF245B FN SS TO92	1				
Q106	R131491	Q BSX20 .2369 N SS TO18	1	R 1	R101543	R MF H 3K9 F 0W4 E3	
Q107	R132926	Q BFQ63 N SS TO72	1	R 2	R101543	R MF H 3K9 F 0W4 E3	
Q108	R132926	Q BFQ63 N SS TO72	1	R 3	R101536	R MF H 1K F 0W4 E3	
Q109	R1314182	Q BC559C P SS TO92		R 4	R101544	R MF H 4K7 F 0W4 E3	
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Q111	R132925	Q BFQ53 N SS TO72	1	R 6	R101550	R MF H 15K F 0W4 E3	
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Q115	R132910	Q BS170 FN SS TO92	1	R 10	R101554	R MF H 33K F 0W4 E3	
Q117	R132925	Q BFQ53 N SS TO72	1	R 11	R101514	R MF H 15E F 0W4 E3	
Q118	R1314295	Q BC549B N SS TO92		R 12	R101538	R MF H 1K5 F 0W4 E3	
Q119	R132911	Q 2N5583 P SS TO39	1	R 13	R101554	R MF H 33K F 0W4 E3	
Q120	R132954	Q BFY90 N SS TO72	1	R 14	R101538	R MF H 1K5 F 0W4 E3	
Q121	R132916	Q BS250 FN SS TO92	1	R 15	R101524	R MF H100E F 0W4 E3	
Q122	R1314181	Q BC559B P SS TO92	1	R 16	R101554	R MF H 33K F 0W4 E3	
Q123	R132944	Q BCY87 2N SS TO71	1	R 17	R101545	R MF H 5K6 F 0W4 E3	
Q124	R1314651	Q BF245B FN SS TO92	1	R 18	R101571	R MF H820K F 0W4 E3	
Q125	R132910	Q BS170 FN SS TO92	1	R 19	R101512	R MF H 10E F 0W4 E3	
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Q201	R131491	Q BSX20 .2369 N SS TO18	1	R 21	R101516	R MF H 22E F 0W4 E3	
Q204	R1314651	Q BF245B FN SS TO92	1	R 22	R1015471	R MF H 7K5 F 0W4 E3	
Q205	R1314651	Q BF245B FN SS TO92	1	R 23	R101546	R MF H 6K8 F 0W4 E3	
Q206	R131491	Q BSX20 .2369 N SS TO18	1	R 24	R101543	R MF H 3K9 F 0W4 E3	
Q207	R132926	Q BFQ63 N SS TO72	1	R 25	R101552	R MF H 22K F 0W4 E3	
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Q209	R1314182	Q BC559C P SS TO92		R 29	R101554	R MF H 33K F 0W4 E3	
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Q221	R132916	Q BS250 FN SS TO92	1	R 40	R101556	R MF H 47K F 0W4 E3	
Q222	R1314181	Q BC559B P SS TO92		R 41	R101560	R MF H100K F 0W4 E3	
Q223	R132944	Q BCY87 2N SS TO71	1	R 42	R1015377	R MF H 24K3 F 0W4 E3	
Q224	R1314651	Q BF245B FN SS TO92	1	R 44	R101523	R MF H 82E F 0W4 E3	
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Q226	R1314295	Q BC549B N SS TO92		R 50	R101534	R MF H680E F 0W4 E3	
				R 51	R101530	R MF H330E F 0W4 E3	

R 52	R101532	R MF H470E F 0W4 E3		R165	R1011129	R CFFH 10E J 0W25	1
R 53	R101512	R MF H 10E F 0W4 E3		R166	R101539	R MF H 1K8 F 0W4 E3	
R 54	R101538	R MF H 1K5 F 0W4 E3		R167	R101536	R MF H 1K F 0W4 E3	
R 55	R101512	R MF H 10E F 0W4 E3		R168	R101544	R MF H 4K7 F 0W4 E3	
R 56	R101538	R MF H 1K5 F 0W4 E3		R169	R101539	R MF H 1K8 F 0W4 E3	
R 57	R101538	R MF H 1K5 F 0W4 E3		R170	R101541	R MF H 2K7 F 0W4 E3	
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R 59	R101559	R MF H 82K F 0W4 E3		R172	R101512	R MF H 10E F 0W4 E3	
R 63	R1015377	R MF H 24K3 F 0W4 E3		R174	R1015765	R MF H619E F 0W4 E3	
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R 70	R101540	R MF H 2K2 F 0W4 E3		R183	R101536	R MF H 1K F 0W4 E3	
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R 76	R101524	R MF H100E F 0W4 E3		R185	R101531	R MF H390E F 0W4 E3	
R 78	R101537	R MF H 1K2 F 0W4 E3		R186	R101542	R MF H 3K3 F 0W4 E3	
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R 80	R101548	R MF H 10K F 0W4 E3		R188	R101550	R MF H 15K F 0W4 E3	
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R101	R101544	R MF H 4K7 F 0W4 E3		R190	R101520	R MF H 47E F 0W4 E3	
R102	R101552	R MF H 22K F 0W4 E3		R201	R101544	R MF H 4K7 F 0W4 E3	
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R107	R101543	R MF H 3K9 F 0W4 E3		R206	R101532	R MF H470E F 0W4 E3	
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R111	R101532	R MF H470E F 0W4 E3		R210	R101526	R MF H150E F 0W4 E3	
R112	R101515	R MF H 18E F 0W4 E3		R211	R101532	R MF H470E F 0W4 E3	
R114	R101524	R MF H100E F 0W4 E3		R212	R101515	R MF H 18E F 0W4 E3	
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R116	R101512	R MF H 10E F 0W4 E3		R215	R101538	R MF H 1K5 F 0W4 E3	
R117	R1015311	R MF H360E F 0W4 E3		R216	R101512	R MF H 10E F 0W4 E3	
R118	R101571	R MF H820K F 0W4 E3		R217	R1015311	R MF H360E F 0W4 E3	
R119	R101562	R MF H150K F 0W4 E3		R218	R101571	R MF H820K F 0W4 E3	
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R137	R101547	R MF H 8K2 F 0W4 E3		R249	R101527	R MF H180E F 0W4 E3	1
R138	R101552	R MF H 22K F 0W4 E3		R250	R1015231	R MF H 75E F 0W4 E3	
R139	R1015231	R MF H 75E F 0W4 E3		R252	R101535	R MF H820E F 0W4 E3	
R140	R101537	R MF H 1K2 F 0W4 E3		R253	R101520	R MF H 47E F 0W4 E3	
R141	R101550	R MF H 15K F 0W4 E3		R254	R101536	R MF H 1K F 0W4 E3	
R149	R101527	R MF H180E F 0W4 E3	1	R255	R101548	R MF H 10K F 0W4 E3	
R150	R1015231	R MF H 75E F 0W4 E3		R256	R1011129	R CFFH 10E J 0W25	1
R152	R101535	R MF H820E F 0W4 E3		R257	R1015281	R MF H200E F 0W4 E3	
R153	R101520	R MF H 47E F 0W4 E3		R258	R101512	R MF H 10E F 0W4 E3	
R154	R101536	R MF H 1K F 0W4 E3		R259	R101512	R MF H 10E F 0W4 E3	
R155	R101548	R MF H 10K F 0W4 E3		R260	R101548	R MF H 10K F 0W4 E3	
R156	R1011129	R CFFH 10E J 0W25	1	R265	R1011129	R CFFH 10E J 0W25	1
R157	R1015281	R MF H200E F 0W4 E3		R266	R101539	R MF H 1K8 F 0W4 E3	
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R159	R101512	R MF H 10E F 0W4 E3		R268	R101544	R MF H 4K7 F 0W4 E3	
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R270	R101541	R MF H 2K7 F0W4 E3		R374	R1015765	R MF H619E F0W4 E3	
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R272	R101512	R MF H 10E F0W4 E3		R376	R101522	R MF H 68E F0W4 E3	
R274	R1015925	R MF H909E F0W4 E3		R377	R101526	R MF H150E F0W4 E3	
R275	R101526	R MF H150E F0W4 E3		R379	R101526	R MF H150E F0W4 E3	
R276	R101522	R MF H 68E F0W4 E3		R380	R101537	R MF H 1K2 F0W4 E3	
R277	R101526	R MF H150E F0W4 E3		R381	R101527	R MF H180E F0W4 E3	1
R278	R101542	R MF H 3K3 F0W4 E3		R382	R101537	R MF H 1K2 F0W4 E3	
R281	R101527	R MF H180E F0W4 E3	1	R383	R101536	R MF H 1K F0W4 E3	
R282	R101537	R MF H 1K2 F0W4 E3		R384	R101537	R MF H 1K2 F0W4 E3	
R283	R101536	R MF H 1K F0W4 E3		R385	R101531	R MF H390E F0W4 E3	
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R285	R101531	R MF H390E F0W4 E3		R387	R1015311	R MF H360E F0W4 E3	
R286	R101542	R MF H 3K3 F0W4 E3		R388	R101550	R MF H 15K F0W4 E3	
R288	R101550	R MF H 15K F0W4 E3		R389	R101508	R MF H 4E7 F0W4 E3	
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R302	R101552	R MF H 22K F0W4 E3					
R305	R101532	R MF H470E F0W4 E3		S 69	R313286	J MO1 C MBT P 3 R1SN 7,5	1
R306	R101532	R MF H470E F0W4 E3		SR60	R1003009	R CFFV 1E J0W25 E1	1
R307	R101543	R MF H 3K9 F0W4 E3		SR61	R1011907	R CFFH E1 J0W4	1
R308	R101528	R MF H220E F0W4 E3		SR62	R1011907	R CFFH E1 J0W4	1
R309	R101536	R MF H 1K F0W4 E3					
R310	R101527	R MF H180E F0W4 E3	1	Z 5	R131740	D ZEN 12V 0W5 C DO34	1
R311	R101532	R MF H470E F0W4 E3		Z 8	R131720	D ZEN 6V2 0W5 C DO35	1
R312	R101515	R MF H 18E F0W4 E3		Z 20	R131754	D ZEN 3V3 0W5 C DO35	1
R314	R101524	R MF H100E F0W4 E3		Z 31	R131716	D ZEN 5V1 0W5 C DO35	1
R315	R101538	R MF H 1K5 F0W4 E3		Z 32	R131733	D STB 2V 0W33 DO35	1
R316	R101512	R MF H 10E F0W4 E3		Z101	R131733	D STB 2V 0W33 DO35	1
R317	R1015311	R MF H360E F0W4 E3		Z103	R131733	D STB 2V 0W33 DO35	1
R318	R101571	R MF H820K F0W4 E3		Z104	R131742	D ZEN 6V8 0W5 C DO35	1
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R320	R101548	R MF H 10K F0W4 E3		Z201	R131733	D STB 2V 0W33 DO35	1
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R334	R101544	R MF H 4K7 F0W4 E3					
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R356	R1011129	R CFFH 10E J0W25	1				
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R358	R101512	R MF H 10E F0W4 E3					
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R370	R101541	R MF H 2K7 F0W4 E3					
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Parts listing R7621175S

SIT.	ITEM NO.	DESCRIPTION	QUANTITY	SIT.	ITEM NO.	DESCRIPTION	QUANTITY
	R3484073	CD CT FTMS P 7 135	1	C458	R112763	C CE MI 10N U100E2	
10	R3631059	SCR D933 M 3 X 8 XIC	2	C459	R113724	C POMERA 100N K 63E2	
20	R366102	NUT D934 M 3 S Z	2	C460	R112763	C CE MI 10N U100E2	
				C461	R113724	C POMERA 100N K 63E2	
50	R801602	X ACC INSUL STRIP PCMS	2	C462	R112739	C CE MI 1N K100E2	
				C463	R112739	C CE MI 1N K100E2	
C400	R111193	C EL AX1000M T 40E18 85	1	C464	R112692	C N750MI 120P J500E2	1
C401	R112368	C N750MI 330P J 63E2	1	C465	R113724	C POMERA 100N K 63E2	
C402	R112774	C CE MI 100N S 63E2		C466	R113724	C POMERA 100N K 63E2	
C403	R112774	C CE MI 100N S 63E2		C467	R113724	C POMERA 100N K 63E2	
C404	R112226	C NPO MI 4P7C 63E2	1	C468	R113730	C POMERA 330N K 63E2	
C405	R112739	C CE MI 1N K100E2		C469	R113730	C POMERA 330N K 63E2	
C406	R112235	C NPO MI 27P G 63E2		C470	R1137121	C POMERA 10N K100E2	
C407	R111510	C EL RA 22M M 25E2 85		C471	R1137121	C POMERA 10N K100E2	
C408	R114102	C POMERA 220N K100E4	1	C472	R113730	C POMERA 330N K 63E2	
C409	R1159101	C PP RA 560P J100E2	1	C473	R113724	C POMERA 100N K 63E2	
C410	R112243	C NPO MI 120P J 63E2		C474	R113724	C POMERA 100N K 63E2	
C411	R112368	C N750MI 330P J 63E2		C475	R113724	C POMERA 100N K 63E2	
C412	R111548	C EL RA 2M2M 50E2 85		C476	R113724	C POMERA 100N K 63E2	
C413	R113730	C POMERA 330N K 63E2		C477	R111476	C EL RA 47M M 25E2 85	
C414	R112774	C CE MI 100N S 63E2		C478	R113724	C POMERA 100N K 63E2	
C415	R112774	C CE MI 100N S 63E2		C479	R112224	C NPO MI 3P3C 63E2	1
C416	R111531	C EL RA 10M M 35E2 85		D400	R1316361	D Y BAT85 030200 DO35	
C417	R111510	C EL RA 22M M 25E2 85		D401	R131621	D S 1N4148 075150 DO35	
C418	R113724	C POMERA 100N K 63E2		D402	R131621	D S 1N4148 075150 DO35	
C419	R111678	C EL BRA 10M M 25E2 85		D403	R131621	D S 1N4148 075150 DO35	
C420	R111477	C EL RA 100M Z 25E2 85		D404	R131621	D S 1N4148 075150 DO35	
C421	R1122415	C NPO MI 82P J 63E2		D405	R131621	D S 1N4148 075150 DO35	
C422	R112243	C NPO MI 120P J 63E2		D406	R131621	D S 1N4148 075150 DO35	
C423	R1159061	C PP RA 390P J100E2		D407	R1316361	D Y BAT85 030200 DO35	1
C424	R112243	C NPO MI 120P J 63E2		D408	R131621	D S 1N4148 075150 DO35	
C425	R112774	C CE MI 100N S 63E2		D409	R131621	D S 1N4148 075150 DO35	
C426	R112739	C CE MI 1N K100E2		D410	R131621	D S 1N4148 075150 DO35	
C427	R1122415	C NPO MI 82P J 63E2		D411	R131646	D R 1N4007 10201A DO41	1
C428	R112739	C CE MI 1N K100E2		D412	R131621	D S 1N4148 075150 DO35	
C429	R112774	C CE MI 100N S 63E2		D413	R131621	D S 1N4148 075150 DO35	
C430	R111678	C EL BRA 10M M 25E2 85		D414	R131621	D S 1N4148 075150 DO35	
C431	R113724	C POMERA 100N K 63E2		D415	R131733	D STB 2V 0W33 DO35	
C432	R111531	C EL RA 10M M 35E2 85		I400	R137546	U 74HCT32 DIP14 P	1
C433	R111531	C EL RA 10M M 35E2 85		I401	R134001	U 7805 TO220 P	1
C434	R111531	C EL RA 10M M 35E2 85		I402	R134114	U 393 LM DIP8 P	1
C435	R117001	CT 7 -35P 160	1	I403	R137552	U 74HCT123 DIP16 P	1
C436	R117001	CT 7 -35P 160	1	I404	R137011	U 74HCT153 DIP16 P	1
C437	R113724	C POMERA 100N K 63E2		I405	R134002	U 7812 TO220 P	1
C438	R1137161	C POMERA 22N K100E2		I406	R132828	U 4650 TDA DIP28 P	1
C439	R1137161	C POMERA 22N K100E2		I407	R134032	U 78L05 TO92 P	1
C440	R113730	C POMERA 330N K 63E2		I408	R132830	U 4670 TDA DIP18 P	1
C441	R112739	C CE MI 1N K100E2		I409	R1328291	U 4661 TDA DIP16 P	1
C442	R1159101	C PP RA 560P J100E2		I410	R132833	U 76013 SC DIP28 P	1
C443	R113720	C POMERA 47N K 63E2		J400	R313928	J CT H MBT P 8 M2SN	1
C444	R111546	C EL RA 1M M 50E2 85		J401	R313952	J CT H MBS P12 M2SN	1
C445	R111546	C EL RA 1M M 50E2 85		L400	R306052	CH RA NS 150 UH	1
C446	R112236	C NPO MI 33P G 63E2		L401	R3060121	CH RA ES 3.3 UH S7 T	1
C447	R112236	C NPO MI 33P G 63E2		L402	R3060241	CH RA ES 10 UH S7 T	1
C448	R112236	C NPO MI 33P G 63E2		L403	R3060281	CH RA ES 15 UH S7 T	1
C449	R111510	C EL RA 22M M 25E2 85		L404	R3060281	CH RA ES 15 UH S7 T	1
C450	R111478	C EL RA 220M M 25E2 85	1	L405	R3061322	CH AX NS 10 UH	1
C451	R111531	C EL RA 10M M 35E2 85		L406	R3060241	CH RA ES 10 UH S7 T	1
C452	R112366	C N750MI 220P J 63E2		MP40	R313729	J PIN TESTEYE	1
C453	R112366	C N750MI 220P J 63E2		MP41	R313729	J PIN TESTEYE	1
C454	R112763	C CE MI 10N U100E2					
C455	R112763	C CE MI 10N U100E2					
C456	R112235	C NPO MI 27P G 63E2					
C457	R112235	C NPO MI 27P G 63E2					

MP42	R313729	J PIN TESTEYE	1	R430	R101550	R MF H 15K F 0W4 E3	
MP43	R313729	J PIN TESTEYE	1	R431	R101548	R MF H 10K F 0W4 E3	
MP44	R313729	J PIN TESTEYE	1	R432	R101512	R MF H 10E F 0W4 E3	
MP45	R313729	J PIN TESTEYE	1	R433	R101525	R MF H120E F 0W4 E3	
MP46	R313729	J PIN TESTEYE	1	R434	R101556	R MF H 47K F 0W4 E3	
MP47	R313729	J PIN TESTEYE	1	R435	R101556	R MF H 47K F 0W4 E3	
MP48	R313729	J PIN TESTEYE	1	R436	R101512	R MF H 10E F 0W4 E3	
MP49	R313729	J PIN TESTEYE	1	R437	R101548	R MF H 10K F 0W4 E3	
P400	R107005	R TCE H500E M 0W5 S7 TS	1	R438	R101540	R MF H 2K2 F 0W4 E3	
PC	R780368	PCS PJ51 G1200 DEC MK2	1	R439	R101540	R MF H 2K2 F 0W4 E3	
Q400	R131411	Q BC549C N SS TO92		R440	R101512	R MF H 10E F 0W4 E3	
Q401	R1314182	Q BC559C P SS TO92		R441	R101538	R MF H 1K5 F 0W4 E3	
Q402	R132910	Q BS170 FN SS TO92	1	R442	R101535	R MF H820E F 0W4 E3	
Q403	R1314182	Q BC559C P SS TO92		R443	R101522	R MF H 68E F 0W4 E3	
Q404	R131411	Q BC549C N SS TO92		R444	R101522	R MF H 68E F 0W4 E3	
Q405	R131411	Q BC549C N SS TO92		R445	R101560	R MF H100K F 0W4 E3	
Q406	R131411	Q BC549C N SS TO92		R446	R101560	R MF H100K F 0W4 E3	
Q407	R131411	Q BC549C N SS TO92		R447	R101512	R MF H 10E F 0W4 E3	
Q408	R1314182	Q BC559C P SS TO92		R448	R101534	R MF H680E F 0W4 E3	
Q409	R1314182	Q BC559C P SS TO92		R449	R101550	R MF H 15K F 0W4 E3	
Q410	R1314182	Q BC559C P SS TO92		R450	R101550	R MF H 15K F 0W4 E3	
Q411	R131411	Q BC549C N SS TO92		R451	R101534	R MF H680E F 0W4 E3	
Q412	R131411	Q BC549C N SS TO92		R452	R101536	R MF H 1K F 0W4 E3	
Q413	R131411	Q BC549C N SS TO92		R453	R101536	R MF H 1K F 0W4 E3	
Q414	R131411	Q BC549C N SS TO92		R454	R101533	R MF H560E F 0W4 E3	
Q415	R131411	Q BC549C N SS TO92		R455	R101520	R MF H 47E F 0W4 E3	
Q416	R131411	Q BC549C N SS TO92		R456	R101552	R MF H 22K F 0W4 E3	
Q417	R131411	Q BC549C N SS TO92		R457	R101562	R MF H150K F 0W4 E3	
Q418	R131411	Q BC549C N SS TO92		R458	R101520	R MF H 47E F 0W4 E3	
Q419	R131411	Q BC549C N SS TO92		R459	R101552	R MF H 22K F 0W4 E3	
Q420	R131411	Q BC549C N SS TO92		R460	R101562	R MF H150K F 0W4 E3	
Q421	R131411	Q BC549C N SS TO92		R461	R101520	R MF H 47E F 0W4 E3	
Q422	R131411	Q BC549C N SS TO92		R462	R101552	R MF H 22K F 0W4 E3	
Q500	R131411	Q BC549C N SS TO92		R463	R101562	R MF H150K F 0W4 E3	
Q501	R132588	Q BC550C N SS TO92	1	R464	R101550	R MF H 15K F 0W4 E3	
R400	R101524	R MF H100E F 0W4 E3		R465	R101548	R MF H 10K F 0W4 E3	
R401	R1015401	R MF H 2K F 0W4 E3		R466	R101550	R MF H 15K F 0W4 E3	
R402	R101512	R MF H 10E F 0W4 E3		R467	R101548	R MF H 10K F 0W4 E3	
R403	R101514	R MF H 15E F 0W4 E3		R468	R101548	R MF H 10K F 0W4 E3	
R404	R101553	R MF H 27K F 0W4 E3		R469	R101548	R MF H 10K F 0W4 E3	
R405	R101552	R MF H 22K F 0W4 E3		R470	R101550	R MF H 15K F 0W4 E3	
R407	R101548	R MF H 10K F 0W4 E3		R471	R101540	R MF H 2K2 F 0W4 E3	
R408	R101533	R MF H560E F 0W4 E3		R472	R101542	R MF H 3K3 F 0W4 E3	
R409	R101548	R MF H 10K F 0W4 E3		R473	R101536	R MF H 1K F 0W4 E3	
R410	R101544	R MF H 4K7 F 0W4 E3		R474	R3481027	WU JUMP 0.51 7.5 ISO	1
R411	R101570	R MF H680K F 0W4 E3		R475	R101530	R MF H330E F 0W4 E3	
R412	R101560	R MF H100K F 0W4 E3		R476	R101524	R MF H100E F 0W4 E3	
R413	R101544	R MF H 4K7 F 0W4 E3		R477	R101534	R MF H680E F 0W4 E3	
R414	R101544	R MF H 4K7 F 0W4 E3		R478	R101544	R MF H 4K7 F 0W4 E3	
R415	R101532	R MF H470E F 0W4 E3		R479	R101550	R MF H 15K F 0W4 E3	
R416	R101550	R MF H 15K F 0W4 E3		R480	R101546	R MF H 6K8 F 0W4 E3	
R417	R101524	R MF H100E F 0W4 E3		R481	R101545	R MF H 5K6 F 0W4 E3	
R418	R101554	R MF H 33K F 0W4 E3		R482	R101544	R MF H 4K7 F 0W4 E3	
R419	R101556	R MF H 47K F 0W4 E3		R483	R101560	R MF H100K F 0W4 E3	
R420	R101539	R MF H 1K8 F 0W4 E3		R484	R101548	R MF H 10K F 0W4 E3	
R421	R101533	R MF H560E F 0W4 E3		R485	R101552	R MF H 22K F 0W4 E3	
R422	R101548	R MF H 10K F 0W4 E3		R486	R101530	R MF H330E F 0W4 E3	
R423	R101544	R MF H 4K7 F 0W4 E3		R487	R101530	R MF H330E F 0W4 E3	
R424	R101564	R MF H220K F 0W4 E3		R488	R101534	R MF H680E F 0W4 E3	
R425	R101550	R MF H 15K F 0W4 E3		R489	R101538	R MF H 1K5 F 0W4 E3	
R426	R101553	R MF H 27K F 0W4 E3		R490	R101540	R MF H 2K2 F 0W4 E3	
R427	R1015501	R MF H 13K F 0W4 E3		R491	R101545	R MF H 5K6 F 0W4 E3	
R428	R101540	R MF H 2K2 F 0W4 E3		R492	R101543	R MF H 3K9 F 0W4 E3	
R429	R101530	R MF H330E F 0W4 E3		R493	R101548	R MF H 10K F 0W4 E3	
				R494	R101548	R MF H 10K F 0W4 E3	
				R495	R101524	R MF H100E F 0W4 E3	
				R496	R101524	R MF H100E F 0W4 E3	
				R497	R101524	R MF H100E F 0W4 E3	

RGB Driver+QUAD Decoder

R7621175

Sub module: Quad decoder R7621175S

R498	R101524	R	MF H100E F 0W4 E3		Z400	R131744	D	ZEN 5V6 0W5 C DO35	
R500	R101544	R	MF H 4K7 F 0W4 E3		Z401	R131757	D	ZEN 3V9 0W5 C DO35	
R501	R101540	R	MF H 2K2 F 0W4 E3		Z402	R131716	D	ZEN 5V1 0W5 C DO35	
R502	R101550	R	MF H 15K F 0W4 E3		Z403	R131716	D	ZEN 5V1 0W5 C DO35	
R503	R101524	R	MF H100E F 0W4 E3		Z404	R131716	D	ZEN 5V1 0W5 C DO35	
R504	R101540	R	MF H 2K2 F 0W4 E3	1	Z405	R131716	D	ZEN 5V1 0W5 C DO35	
SR40	R1011009	R	CFFH 1E J 0W25	1					
SR41	R1011049	R	CFFH 2E2 J 0W25	1					
SR42	R1011049	R	CFFH 2E2 J 0W25	1					
SR43	R1011049	R	CFFH 2E2 J 0W25	1					
SR44	R1011049	R	CFFH 2E2 J 0W25	1					
SR45	R1011049	R	CFFH 2E2 J 0W25	1					
X400	R306849	X	7.159090 MHZ HC49 S20	1					
X401	R306816	X	8.867238 MHZ HC49 S20	1					

